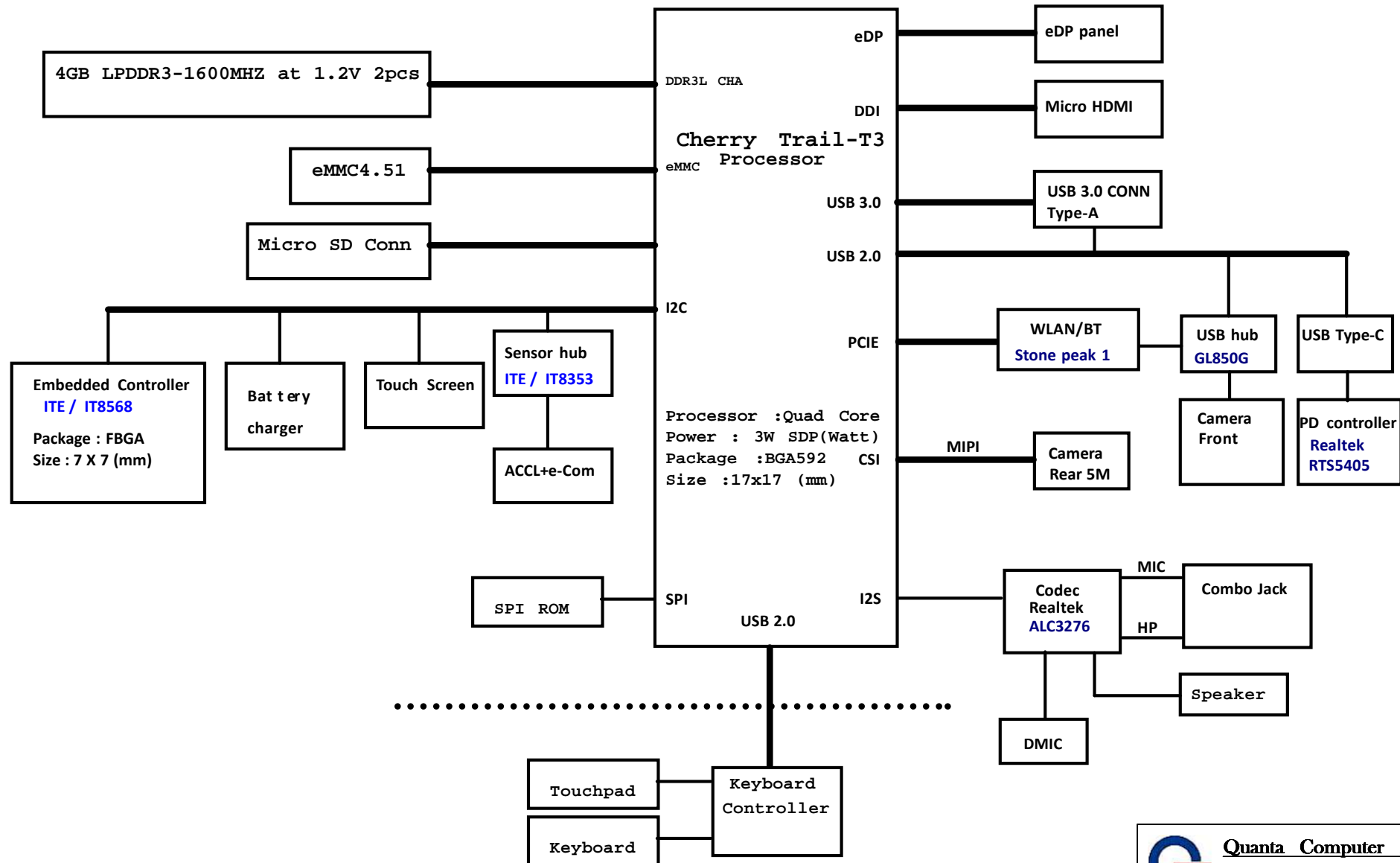


Sweet Cherry Trail T3 Block Diagram

01



3.4 Cherry Trail Power Map

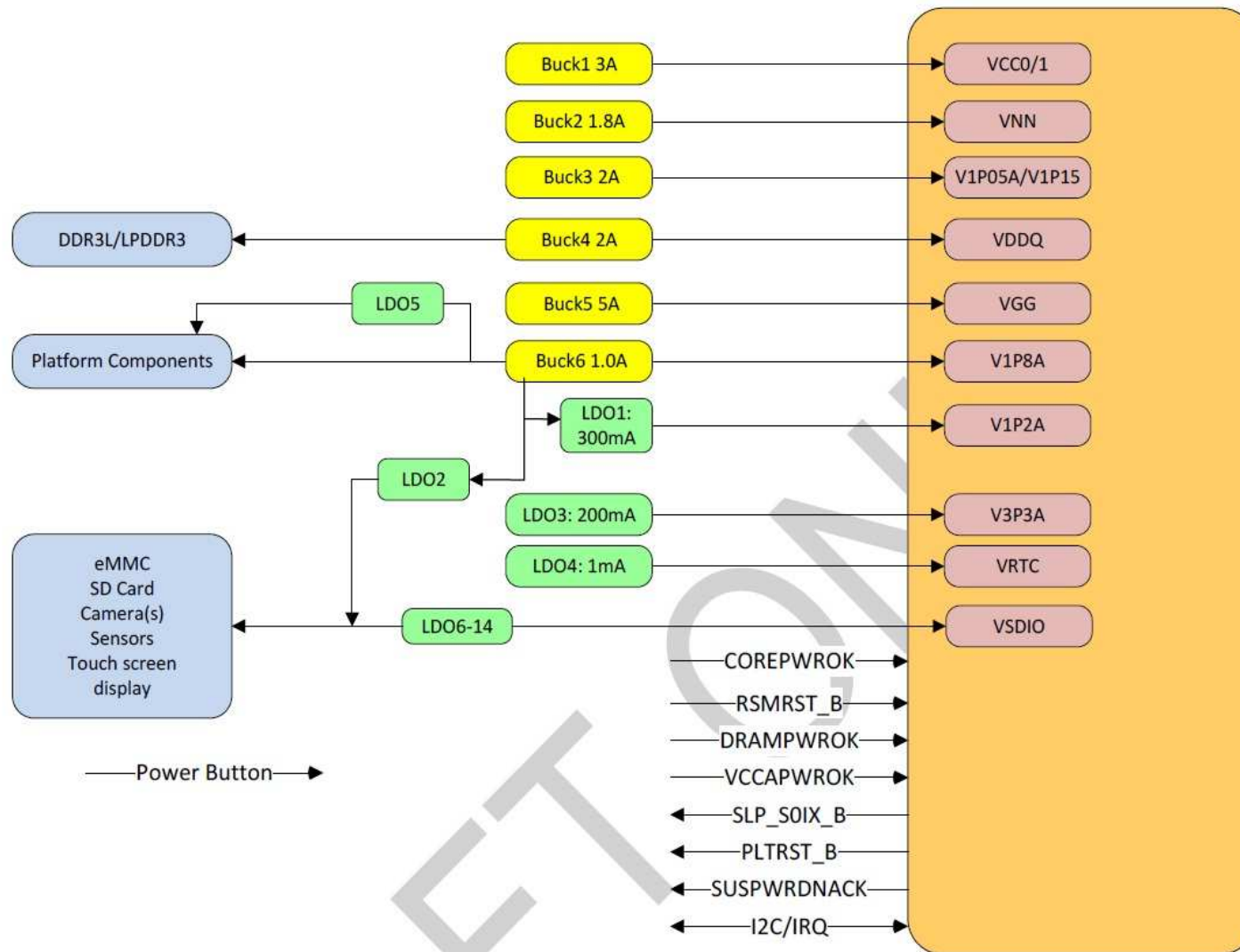
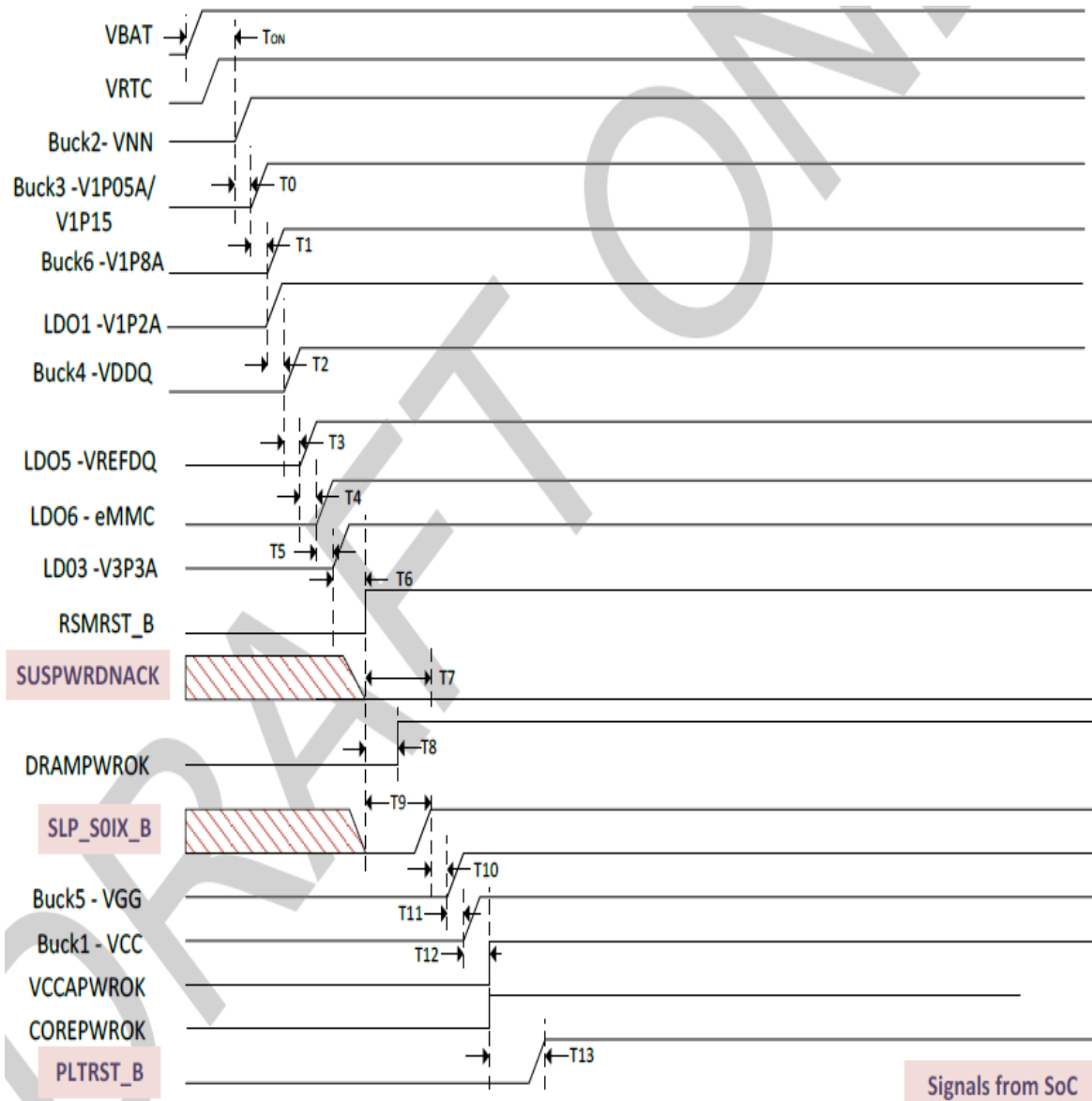


Figure 3-2: Cherry Trail Power Map



Parameter	Description	Min	Typ	Max	Units
T _{ON}	VBAT_PUP to BUCK2 Turn-On Delay		110		ms
T0	BUCK2 to BUCK3 turn on delay (BUCK2 to BUCK3 turn on delay should follow the standard delay (T0), but have an option to support no delay (to be compliant with CHT A0))		300		us
T1	BUCK3 Rail to Subsequent BUCK6 and LDO1 Rail Turn-On Delay		2		ms
T2	BUCK6 and LDO1 Rail to Subsequent BUCK4 Rail Turn-On Delay		2		ms
T3	LDO1 Rail to Subsequent LDO5 Rail Turn-On Delay		2		ms
T4	LDO5 Rail to Subsequent LDO6 Rail Turn-On Delay		2		ms
T5	LDO6 Rail to Subsequent LDO3 Rail Turn-On Delay		2		ms
T6	LDO3 Turn-On Delay to RSMRSTB de-assertion		2		ms
T7	SUSPWRDNACK de-assertion (LOW) to RSMRSTB de-assertion	0			us
T8	RSMRSTB de-assertion to DRAMPWROK assertion	0		100	us
T9	RSMRSTB de-assertion to SLP_S0IXB de-assertion	20			us
T10	SLP_S0IXB de-assertion to first subsequent voltage rail (BUCK5) start to turn-on delay	0	24	100	us
T11	BUCK5 Rail to Subsequent BUCK1 Rail Turn-On Delay		2		ms
T12	BUCK1 Rail Turn-On Delay to VCCAPWROK and COREPWROK assertion		2		ms
T13	COREPWROK assertion to PLTRSTB de-assertion	60			ms



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POWER SEQUENCE

Rev.
1A

Date:

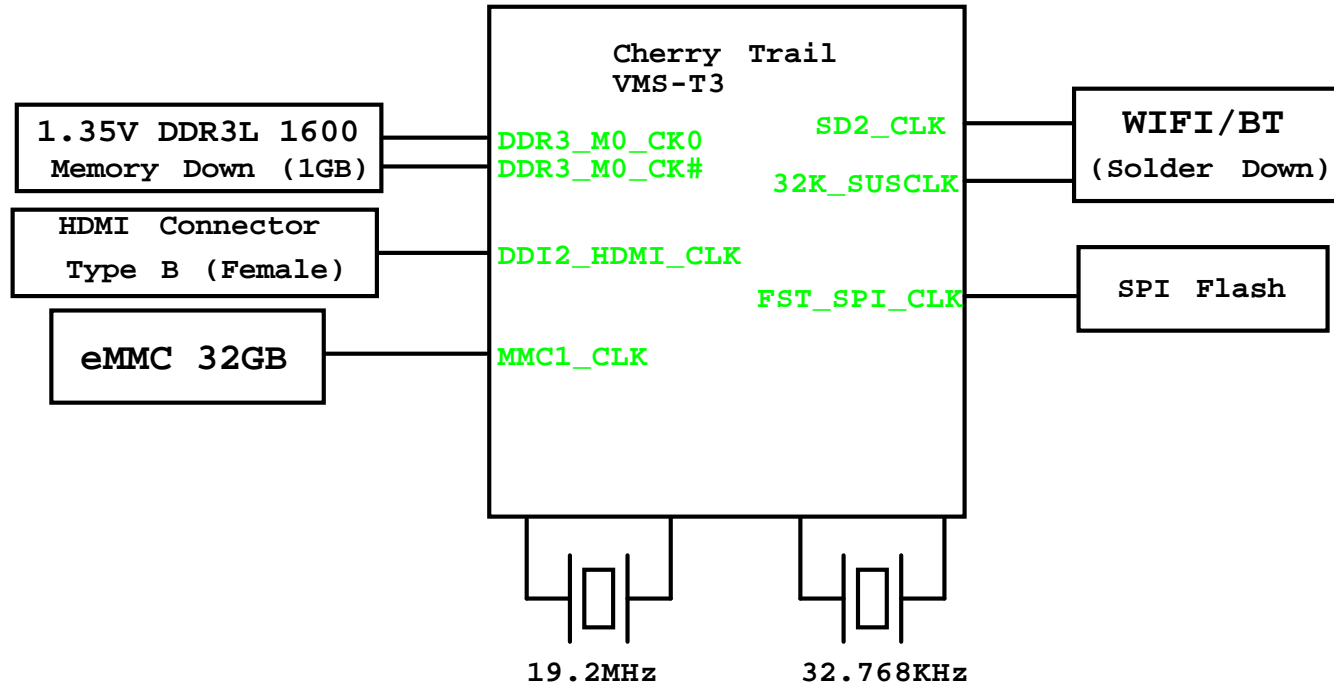
Wednesday, March 09, 2016

Sheet :

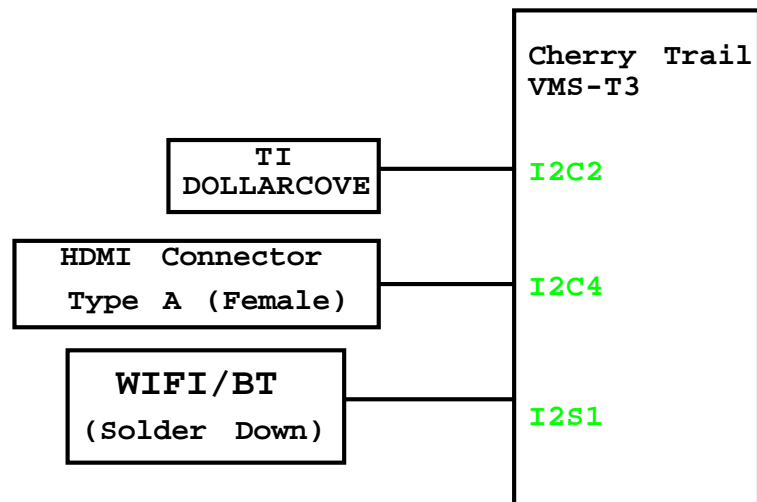
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CLOCK MAP

04



I2C MAP



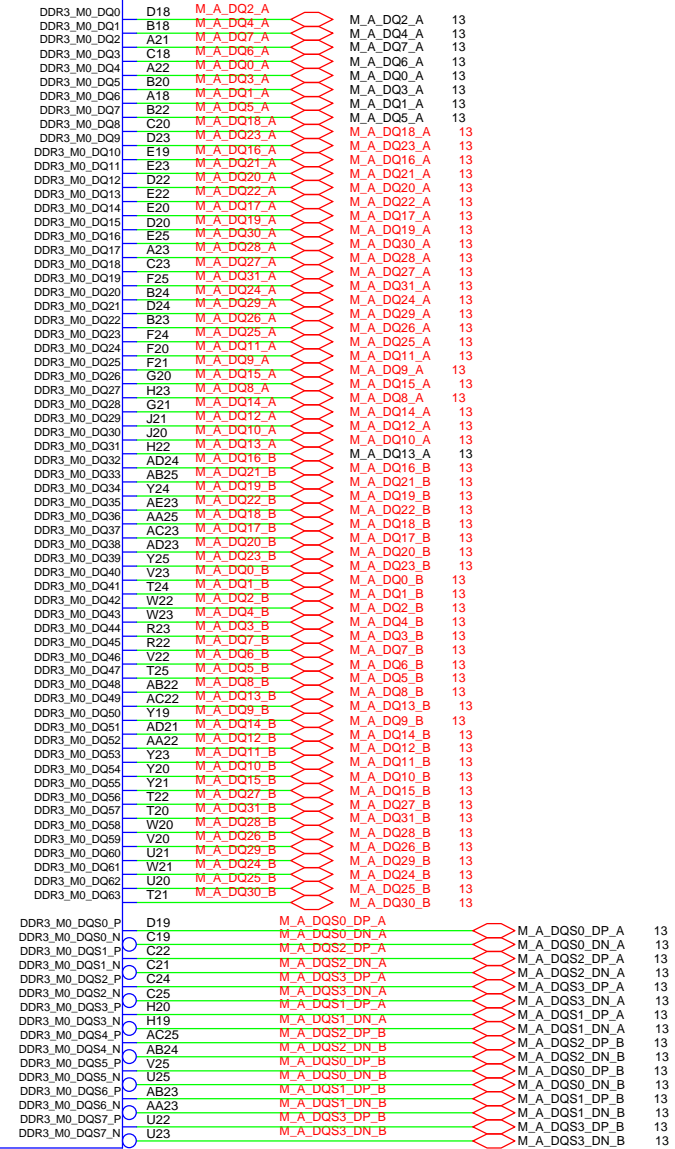
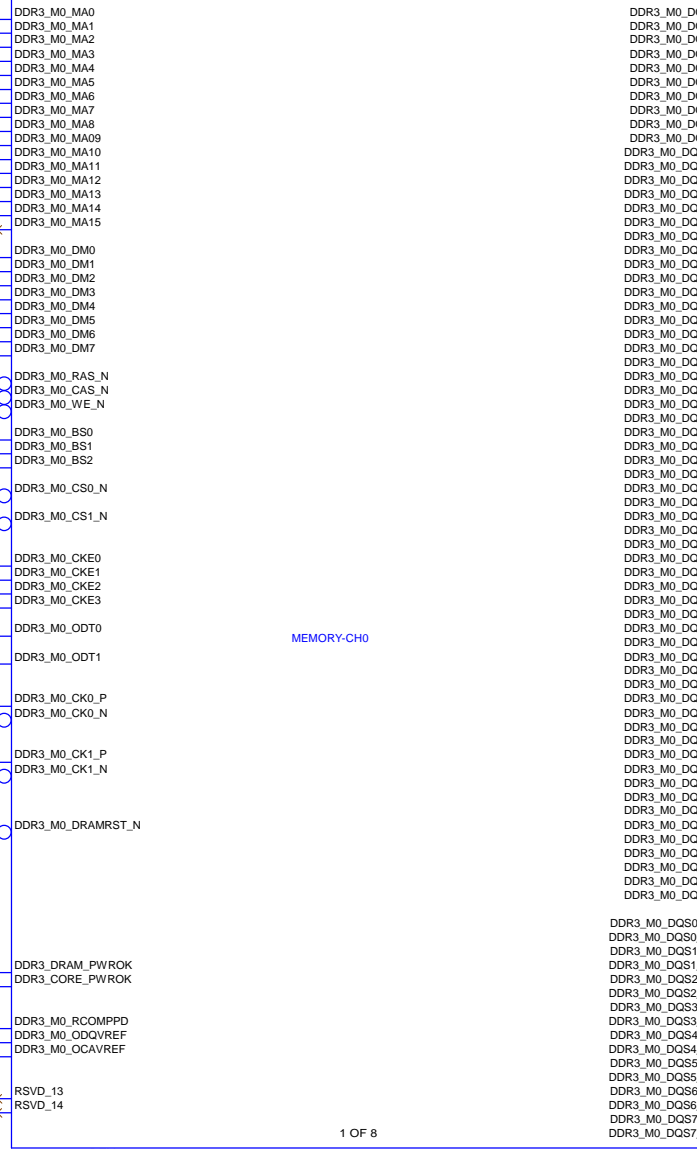
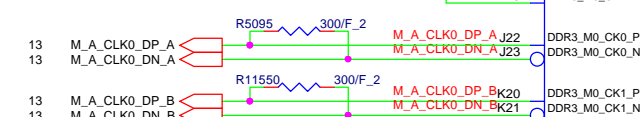
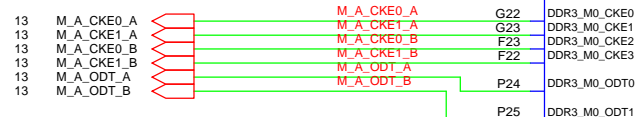
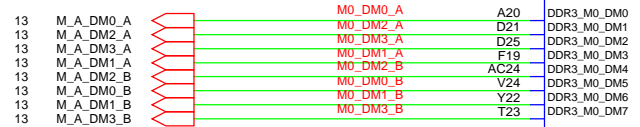
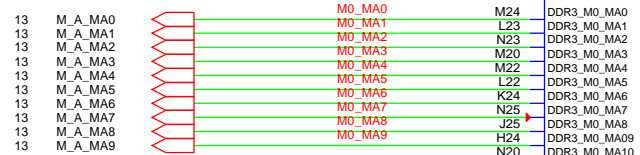
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Size	Document Number	Rev.
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Date:	Wednesday, March 09, 2016	Sheet : 4 of 41

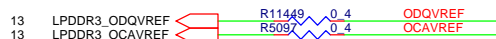
SOC:MEMORY


U1A
SOC_CHV



REV = 0.71

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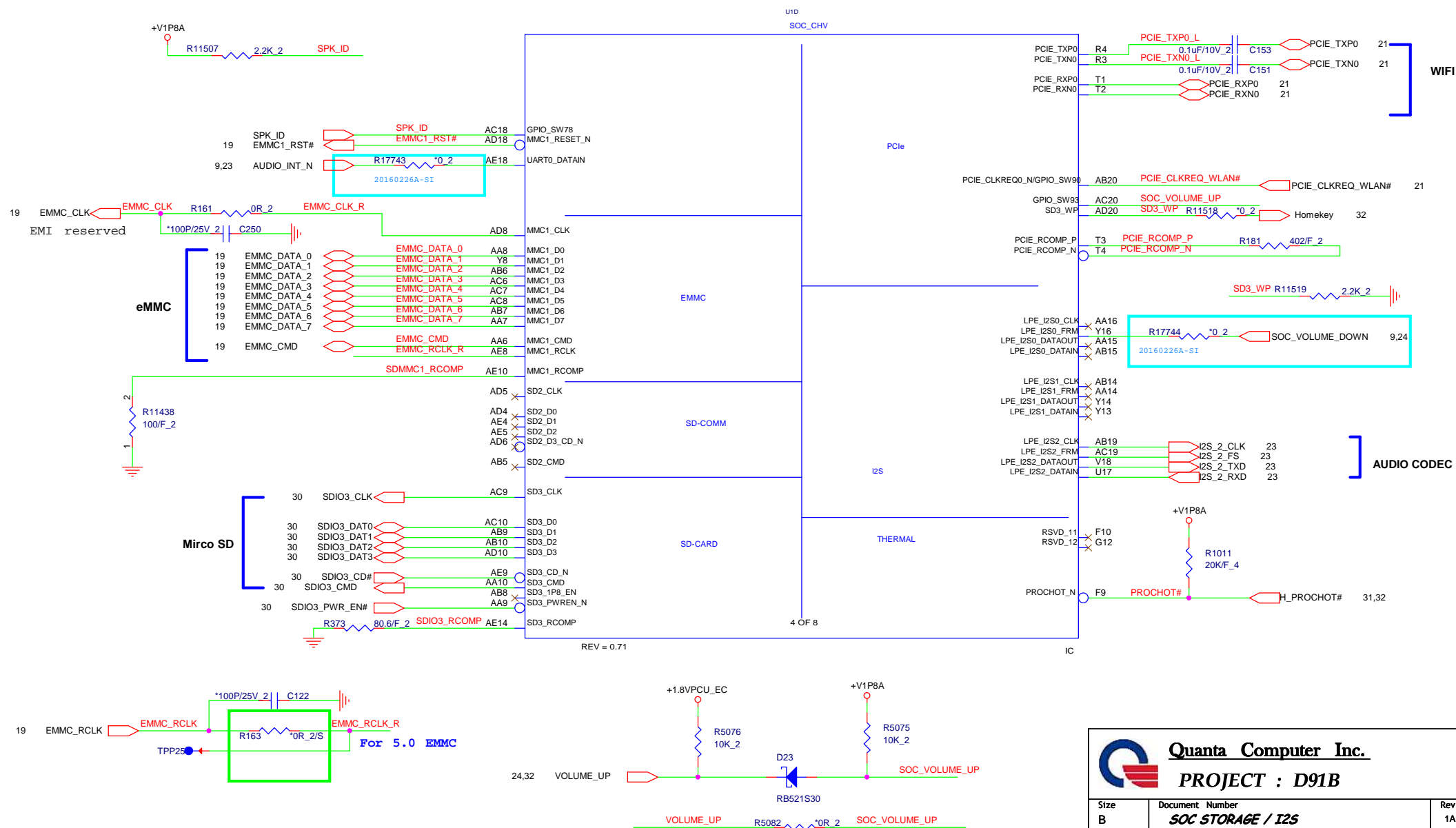
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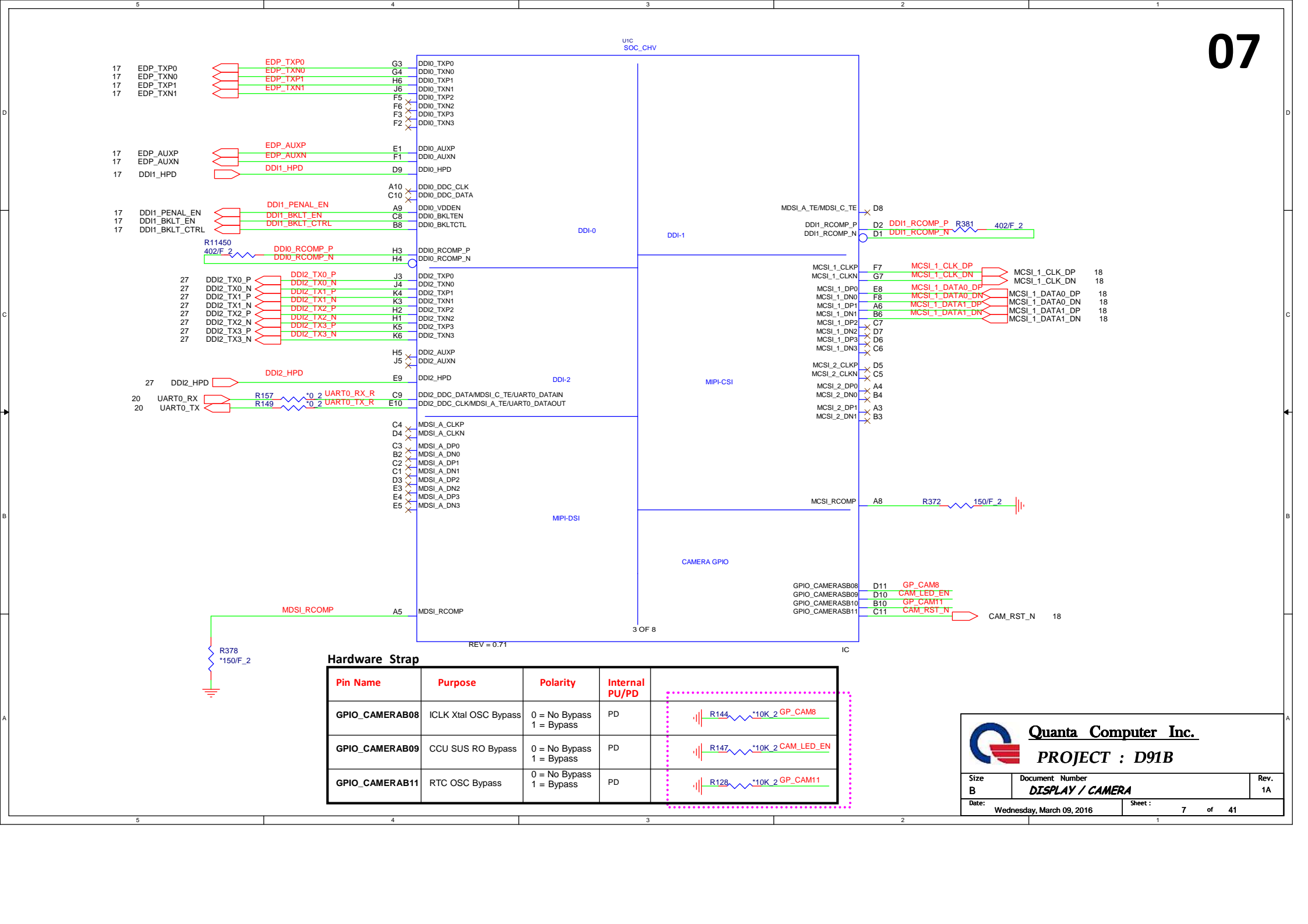
Size	Document Number	Rev.
Custom	SOC MEMORY	1A
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Size B	Document Number <i>SOC STORAGE / I2S</i>	Rev. 1A
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07

UIC SOC_CHV

DDI-0 DDI-1 MIPI-CSI MIPI-DSI CAMERA GPIO

REV = 0.71 3 OF 8 IC

GPIO_CAMERASB08 GPIO_CAMERASB09 GPIO_CAMERASB10 GPIO_CAMERASB11

D11 GP_CAM8 D10 CAM_LED_EN B10 GP_CAM11 C11 CAM_RST_N

CAM_RST_N 18

R378 *150/F_2

R11450 402/F_2 R149 *0 2 R157 *0 2

UART0_RX UART0_TX

DDI2_HPDP

DDI2_TX0_P DDI2_TX0_N DDI2_TX1_P DDI2_TX1_N DDI2_TX2_P DDI2_TX2_N DDI2_TX3_P DDI2_TX3_N

DDI2_AUXP DDI2_AUXN

DDI2_HPDP

DDI1_PENAL_EN DDI1_BKLT_EN DDI1_BKLT_CTRL

DDI1_HPDP

EDP_AUXP EDP_AUXN

EDP_TXP0 EDP_TXN0 EDP_TXP1 EDP_TXN1

DDI0_TXP0 DDI0_TXN0 DDI0_TXP1 DDI0_TXN1 DDI0_TXP2 DDI0_TXN2 DDI0_TXP3 DDI0_TXN3

DDI0_AUXP DDI0_AUXN

DDI0_HPDP

DDI0_DDC_CLK DDI0_DDC_DATA

DDI0_VDDEN DDI0_BKLTEN DDI0_BKLTCTL

DDI0_RCOMP_P DDI0_RCOMP_N

DDI2_TX0_P DDI2_TX0_N DDI2_TX1_P DDI2_TX1_N DDI2_TX2_P DDI2_TX2_N DDI2_TX3_P DDI2_TX3_N

DDI2_AUXP DDI2_AUXN

DDI2_HPDP

DDI2_DDC_DATA/MDSI_C_TE/UART0_DATAIN DDI2_DDC_CLK/MDSI_A_TE/UART0_DATAOUT

MDSI_A_CLKP MDSI_A_CLKN MDSI_A_DP0 MDSI_A_DP1 MDSI_A_DP2 MDSI_A_DP3 MDSI_A_DP4 MDSI_A_DP5 MDSI_A_DP6 MDSI_A_DP7 MDSI_A_DP8 MDSI_A_DP9 MDSI_A_DP10 MDSI_A_DP11 MDSI_A_DP12 MDSI_A_DP13 MDSI_A_DP14 MDSI_A_DP15 MDSI_A_DP16 MDSI_A_DP17 MDSI_A_DP18 MDSI_A_DP19 MDSI_A_DP20 MDSI_A_DP21 MDSI_A_DP22 MDSI_A_DP23 MDSI_A_DP24 MDSI_A_DP25 MDSI_A_DP26 MDSI_A_DP27 MDSI_A_DP28 MDSI_A_DP29 MDSI_A_DP30 MDSI_A_DP31 MDSI_A_DP32 MDSI_A_DP33 MDSI_A_DP34 MDSI_A_DP35 MDSI_A_DP36 MDSI_A_DP37 MDSI_A_DP38 MDSI_A_DP39 MDSI_A_DP40 MDSI_A_DP41 MDSI_A_DP42 MDSI_A_DP43 MDSI_A_DP44 MDSI_A_DP45 MDSI_A_DP46 MDSI_A_DP47 MDSI_A_DP48 MDSI_A_DP49 MDSI_A_DP50 MDSI_A_DP51 MDSI_A_DP52 MDSI_A_DP53 MDSI_A_DP54 MDSI_A_DP55 MDSI_A_DP56 MDSI_A_DP57 MDSI_A_DP58 MDSI_A_DP59 MDSI_A_DP60 MDSI_A_DP61 MDSI_A_DP62 MDSI_A_DP63 MDSI_A_DP64 MDSI_A_DP65 MDSI_A_DP66 MDSI_A_DP67 MDSI_A_DP68 MDSI_A_DP69 MDSI_A_DP70 MDSI_A_DP71 MDSI_A_DP72 MDSI_A_DP73 MDSI_A_DP74 MDSI_A_DP75 MDSI_A_DP76 MDSI_A_DP77 MDSI_A_DP78 MDSI_A_DP79 MDSI_A_DP80 MDSI_A_DP81 MDSI_A_DP82 MDSI_A_DP83 MDSI_A_DP84 MDSI_A_DP85 MDSI_A_DP86 MDSI_A_DP87 MDSI_A_DP88 MDSI_A_DP89 MDSI_A_DP90 MDSI_A_DP91 MDSI_A_DP92 MDSI_A_DP93 MDSI_A_DP94 MDSI_A_DP95 MDSI_A_DP96 MDSI_A_DP97 MDSI_A_DP98 MDSI_A_DP99

MDSI_A_TE/MDSI_C_TE

DDI1_RCOMP_P DDI1_RCOMP_N

MDSI_1_CLKP MDSI_1_CLKN MDSI_1_DP0 MDSI_1_DP1 MDSI_1_DP2 MDSI_1_DP3 MDSI_1_DP4 MDSI_1_DP5 MDSI_1_DP6 MDSI_1_DP7 MDSI_1_DP8 MDSI_1_DP9 MDSI_1_DP10 MDSI_1_DP11 MDSI_1_DP12 MDSI_1_DP13 MDSI_1_DP14 MDSI_1_DP15 MDSI_1_DP16 MDSI_1_DP17 MDSI_1_DP18 MDSI_1_DP19 MDSI_1_DP20 MDSI_1_DP21 MDSI_1_DP22 MDSI_1_DP23 MDSI_1_DP24 MDSI_1_DP25 MDSI_1_DP26 MDSI_1_DP27 MDSI_1_DP28 MDSI_1_DP29 MDSI_1_DP30 MDSI_1_DP31 MDSI_1_DP32 MDSI_1_DP33 MDSI_1_DP34 MDSI_1_DP35 MDSI_1_DP36 MDSI_1_DP37 MDSI_1_DP38 MDSI_1_DP39 MDSI_1_DP40 MDSI_1_DP41 MDSI_1_DP42 MDSI_1_DP43 MDSI_1_DP44 MDSI_1_DP45 MDSI_1_DP46 MDSI_1_DP47 MDSI_1_DP48 MDSI_1_DP49 MDSI_1_DP50 MDSI_1_DP51 MDSI_1_DP52 MDSI_1_DP53 MDSI_1_DP54 MDSI_1_DP55 MDSI_1_DP56 MDSI_1_DP57 MDSI_1_DP58 MDSI_1_DP59 MDSI_1_DP60 MDSI_1_DP61 MDSI_1_DP62 MDSI_1_DP63 MDSI_1_DP64 MDSI_1_DP65 MDSI_1_DP66 MDSI_1_DP67 MDSI_1_DP68 MDSI_1_DP69 MDSI_1_DP70 MDSI_1_DP71 MDSI_1_DP72 MDSI_1_DP73 MDSI_1_DP74 MDSI_1_DP75 MDSI_1_DP76 MDSI_1_DP77 MDSI_1_DP78 MDSI_1_DP79 MDSI_1_DP80 MDSI_1_DP81 MDSI_1_DP82 MDSI_1_DP83 MDSI_1_DP84 MDSI_1_DP85 MDSI_1_DP86 MDSI_1_DP87 MDSI_1_DP88 MDSI_1_DP89 MDSI_1_DP90 MDSI_1_DP91 MDSI_1_DP92 MDSI_1_DP93 MDSI_1_DP94 MDSI_1_DP95 MDSI_1_DP96 MDSI_1_DP97 MDSI_1_DP98 MDSI_1_DP99

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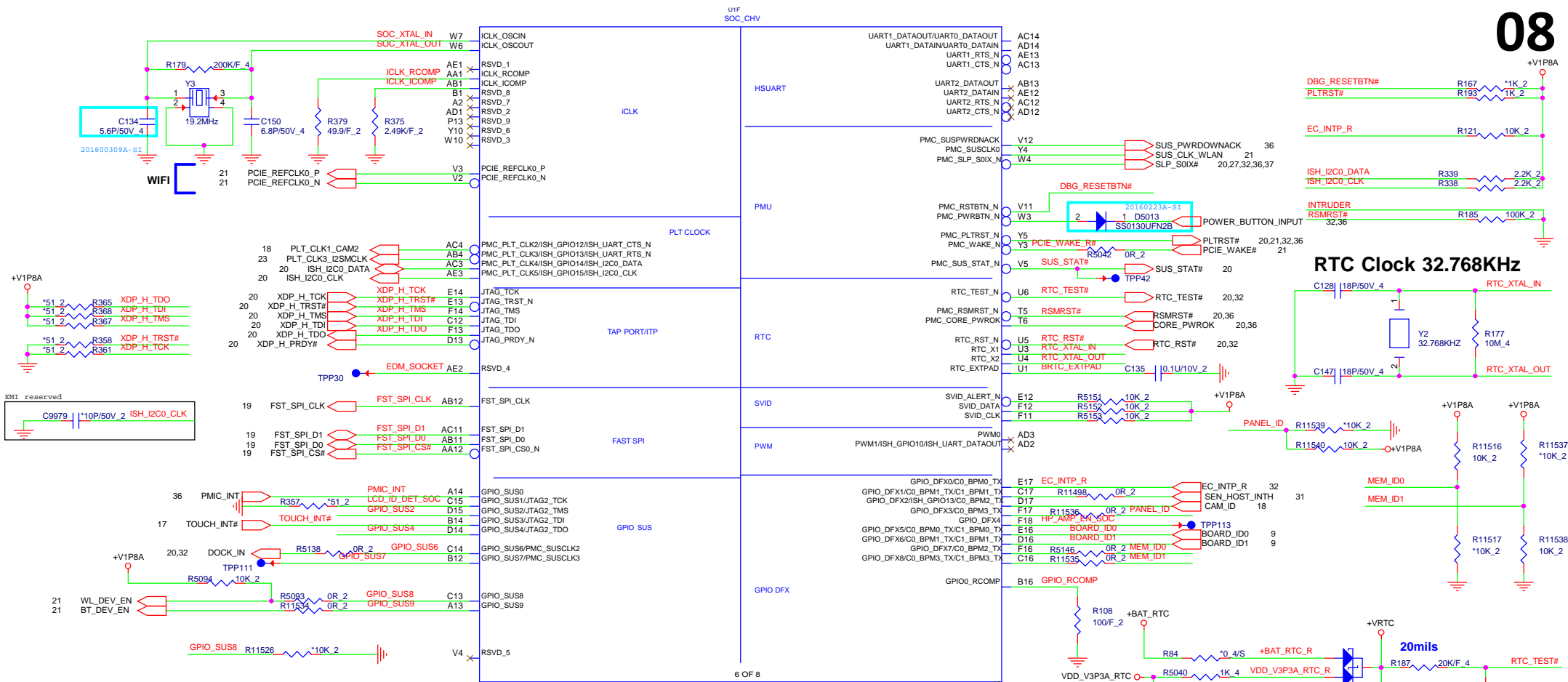
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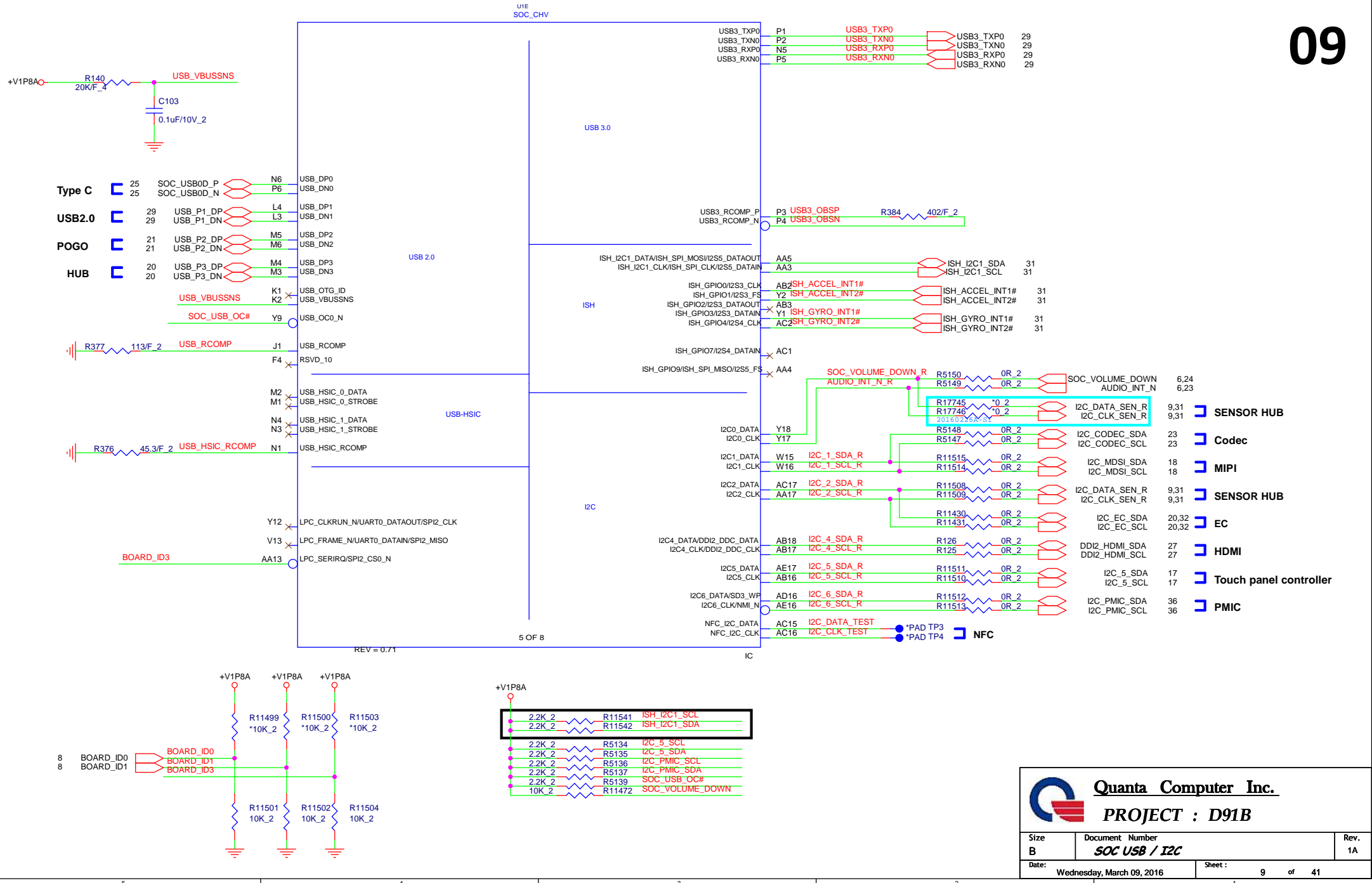
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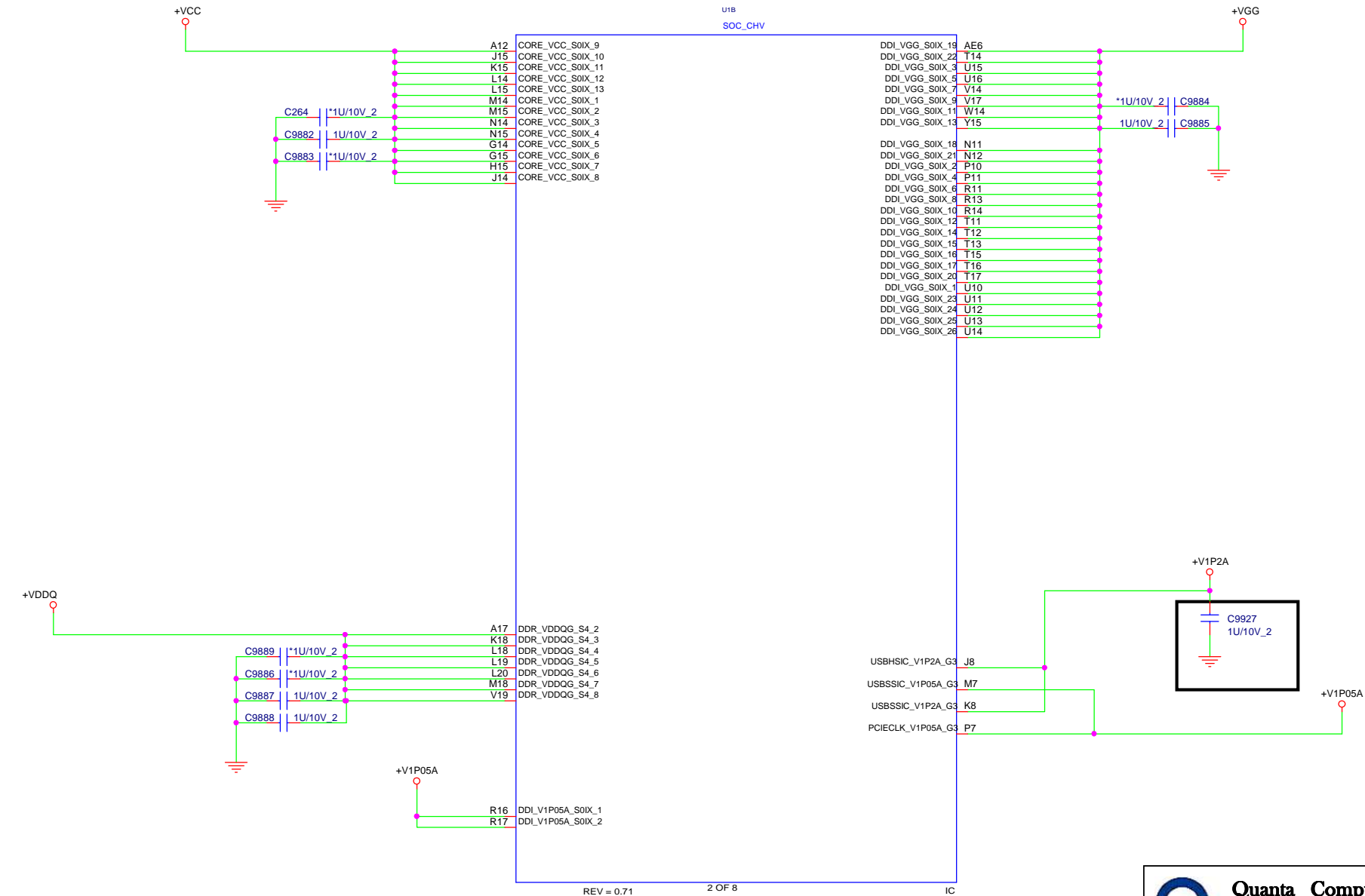




	BALL	C16	F16	AA13	D16	E16
	GPIO	N6	N2	SE79	N8	N4
	PU PD	R11537 R11538	R11516 R11517	R11503 R11504	R11500 R11502	R11499 R11501
	NET	MEM_ID1	MEM_ID0	BOARD_ID3	BOARD_ID1	BOARD_ID0
DDR3L-2GB	Samsung (TH) K4B4G1646E-BYK0	0	1	0	0	0
DDR3L-2GB	Hynix (TG) H5TC4G63CFR-PBA	0	1	0	0	1
DDR3L-4GB	Hynix (TG) H5TC8G63CMR-PBA					
DDR3L-4GB	MT41K256M16TW-107:P					
DDR3L-2GB	Micron (TF) MT41K256M16TW-107:P	0	1	1	0	0
reseve	reseve	0	0	1	0	1


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- 35 +VCC
- 12,13,35,37 +VDDQ
- 35 +VGG



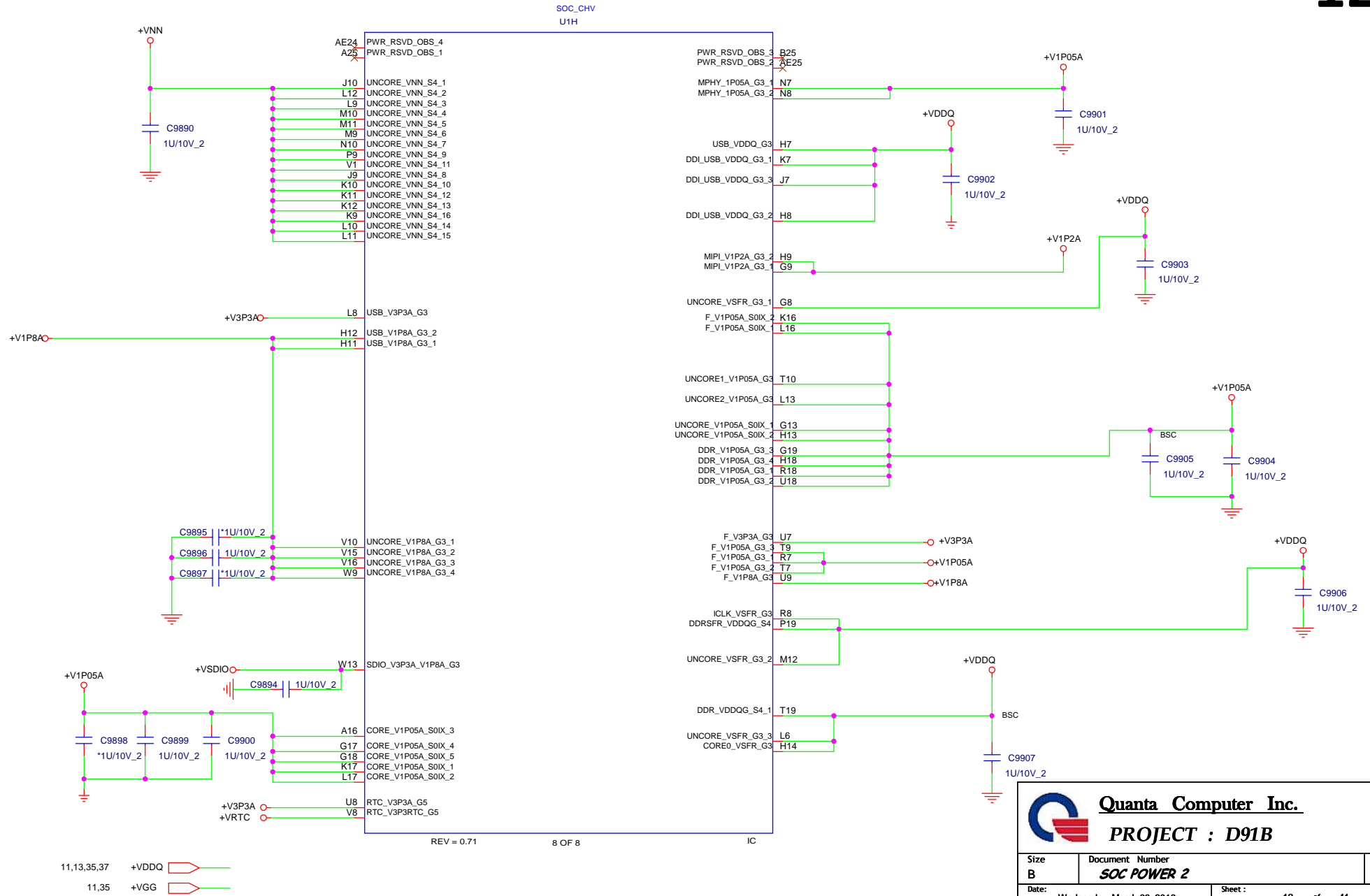
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SOC : POWER 2

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IC

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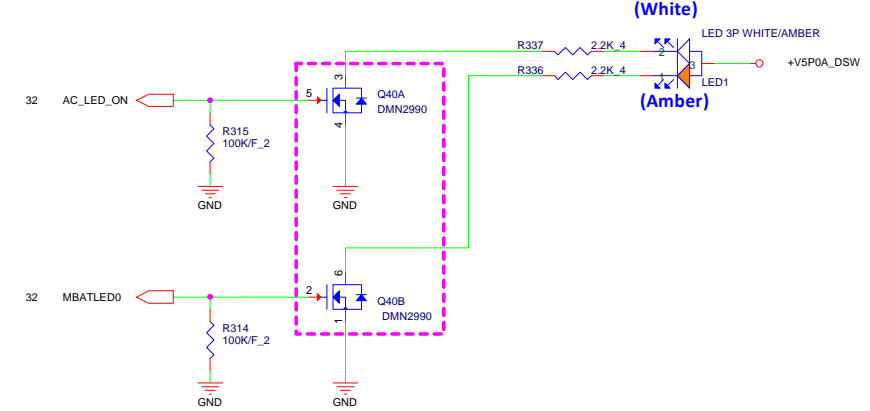
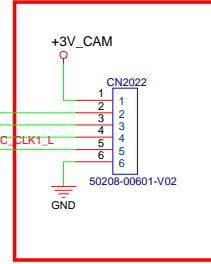
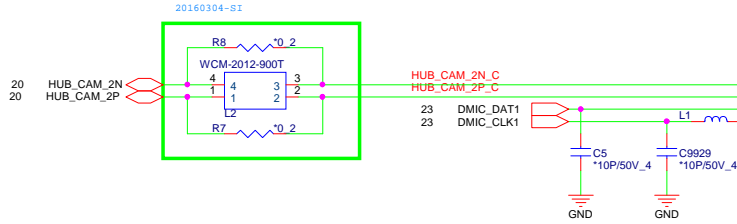
Quanta Computer Inc.

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Suyin 2.0M Webcam / DMIC

18



Front USB HD RGBIR camera only for 12".

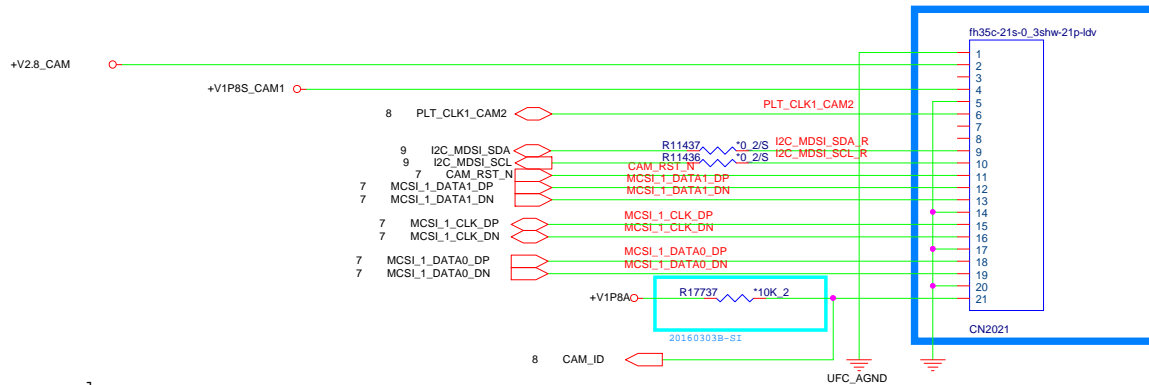
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20 HUB_CAM_3P

TP2110
TP2111

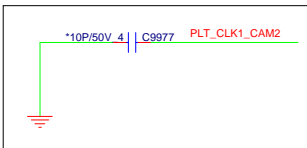
4/20: PN&FP-->OK


pin define need check

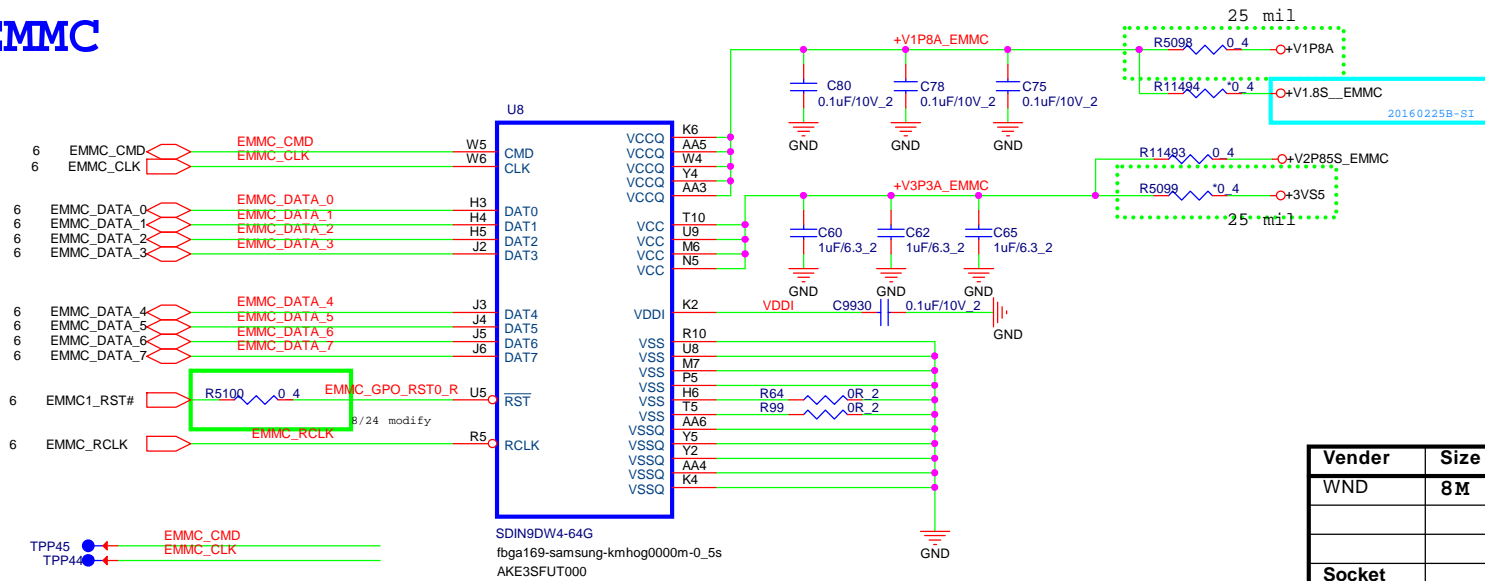
Chicony Camera (Rear 5M)



EMI reserved



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Size Custom	Document Number DMIC/LED/CAM	Rev. 1A
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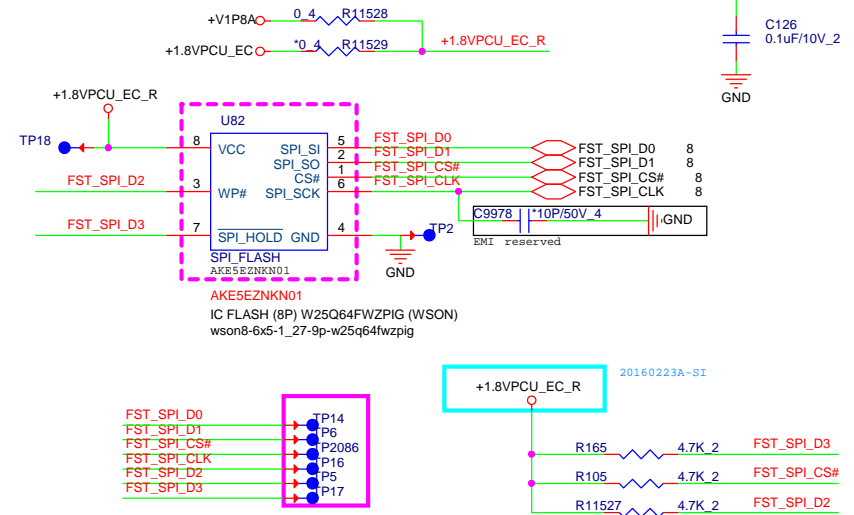



footprint : BGA 169 BGA 153
 BGA 169 PIN :14 mmX18 mm
 BGA 169 PIN :12 mmX16 mm
 BGA 153 PIN :11.5 mmX13 mm

iNAND (eMMC 5.0)

QBCON	TOPB/S	Vender PN	SIZE	
AKE5SZ0T507	AKE5SZ0T506	KLMBG4GEAC-B031	32G	Samsung
AKE3TZPT516	AKE3TZPT515	KLMSG8GEAC-B031	64G	Samsung
AKE3SZ-TW02	AKE3SZ-TW01	H26M64103EMR	32G	Hynix
AKE3TG-TW02	AKE3TG-TW01	H26M78103CCR	64G	Hynix
eMMC 4.51 AKE3UFPT103	AKE3UFPT102	SDIN8CE4-128G	128G	Sandisk
AKE3SFUT001	AKE3SFUT000	SDIN9DW4-32G	32G	Sandisk
AKE3TFUT102	AKE3TFUT101	SDIN9DW4-64G	64G	Sandisk

Vender	Size	P/N
WND	8M	AKE5EZKN01
Socket		DFHS08FS046




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Size	Document Number	Rev.
Custom	eMMC/SPI ROM	1A
Date:	Thursday, March 10, 2016	Sheet : 19 of 41

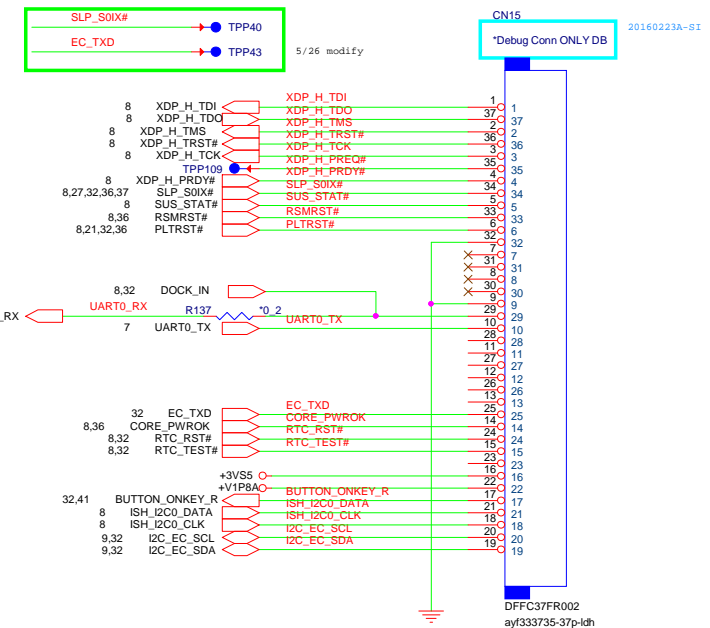
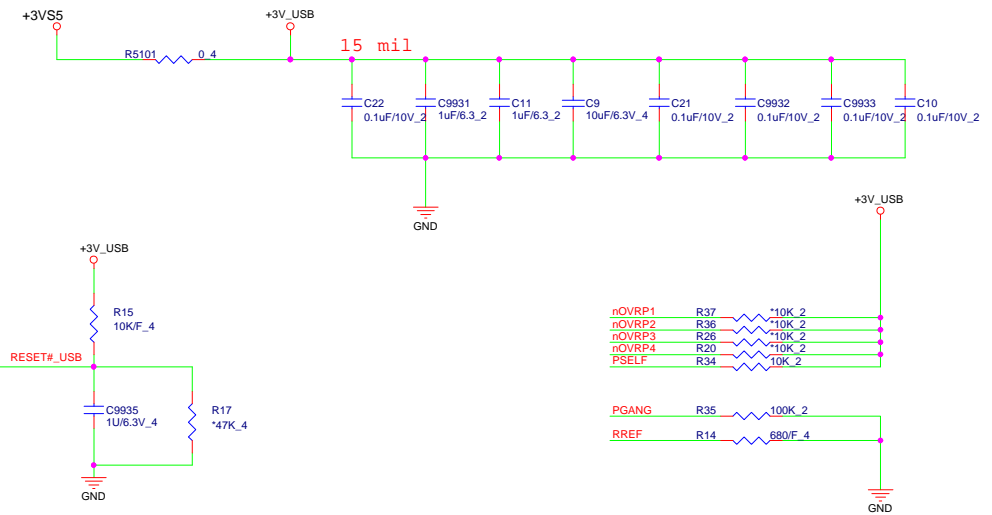
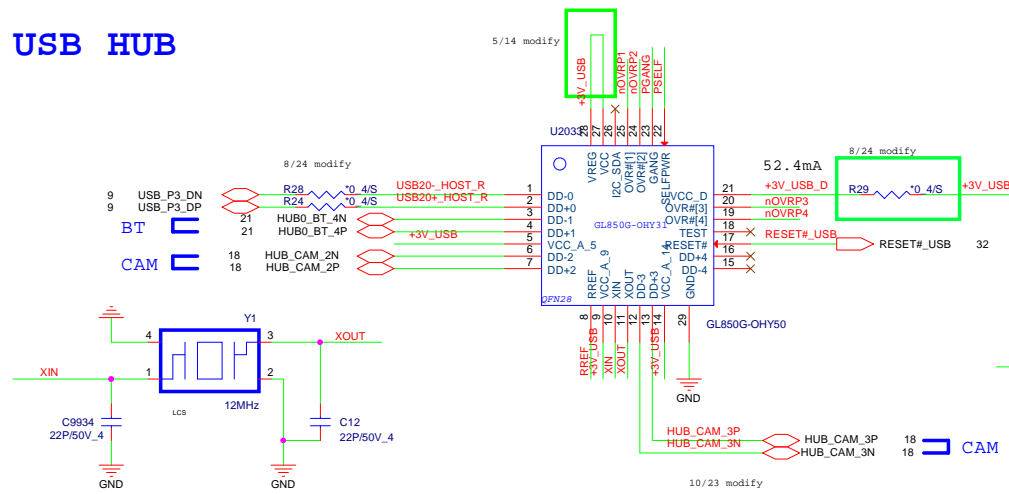
17,18,20,21,23,30,31,37,38
 6,8,9,12,13,17,18,20,21,23,27,31,32,35,36,38

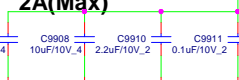
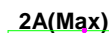
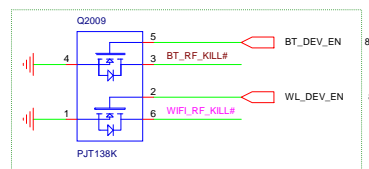
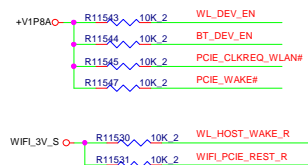
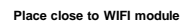
+3VS5
 +V1P8A




USB HUB

20





	KB/TP w/o Wireless charging				KB/TP with Wireless charging			
DOCK_DET	Slate_PU (ohm)	Base_PD (ohm)	DET_EC (V)	+5V_IN (V)	Slate_PU (ohm)	Base_PD (ohm)	DET_EC (V)	+5V_IN (V)
Stand alone	10K	N/A	+1.8V	0V	10K	N/A	+1.8V	0V
Normally	10K	3K	+0.415	+5V	10K	3K	+0.415	+5V
Reversely	10K	20K	+1.2V	0V	10K	1.8K	+0.275V	+5V

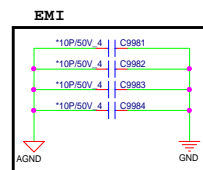
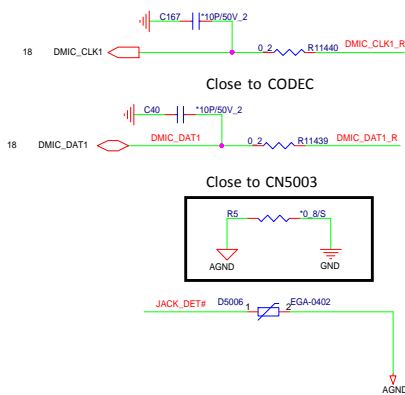
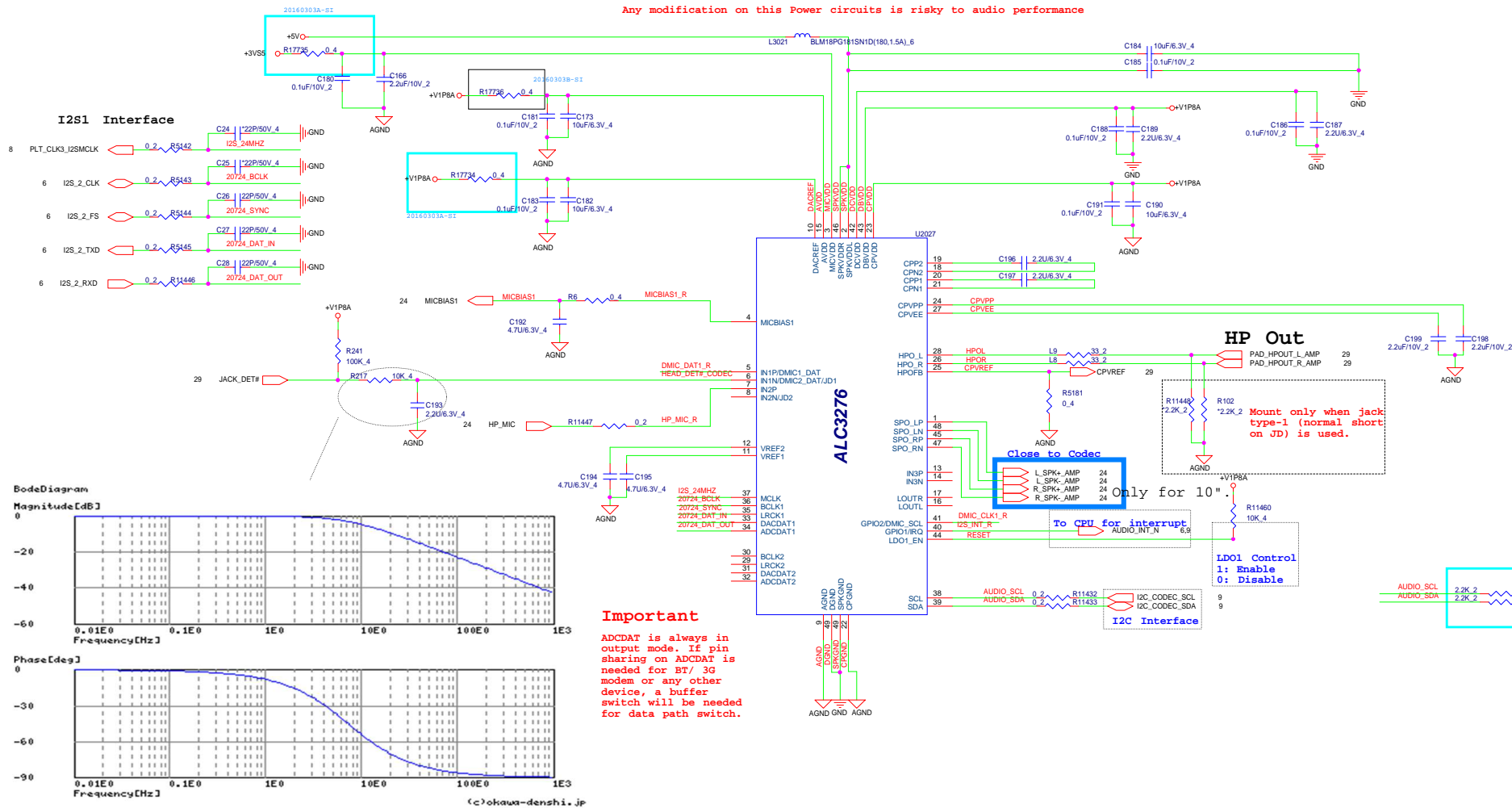


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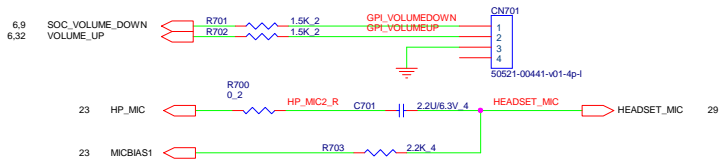
PROJECT : D91B

Size	Document Number	Rev.
Custom	WWAN Conn & SIM	1A
Date:	Wednesday, March 09, 2016	Sheet : 22 of 41

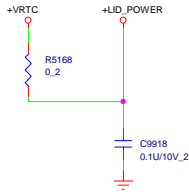
By-Pass Capacitors should be put near the power pins
Any modification on this Power circuits is risky to audio performance



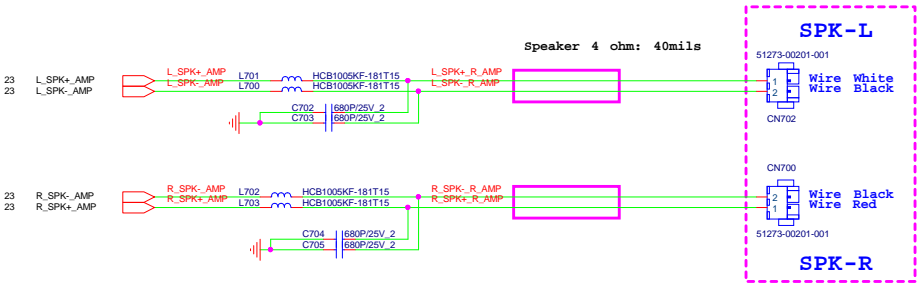
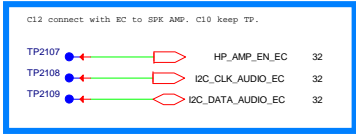
Head Phone out AMPLIFIER

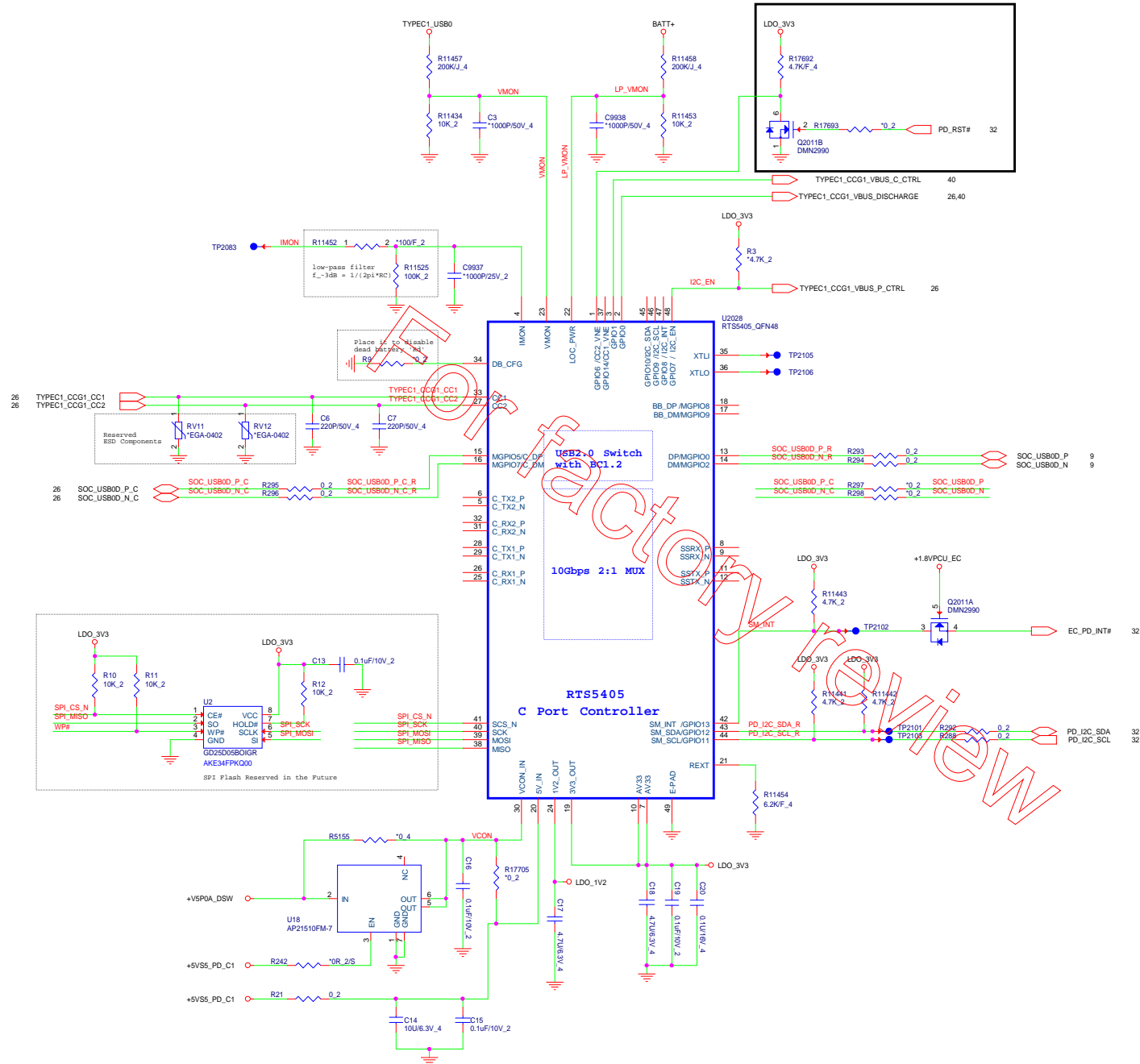


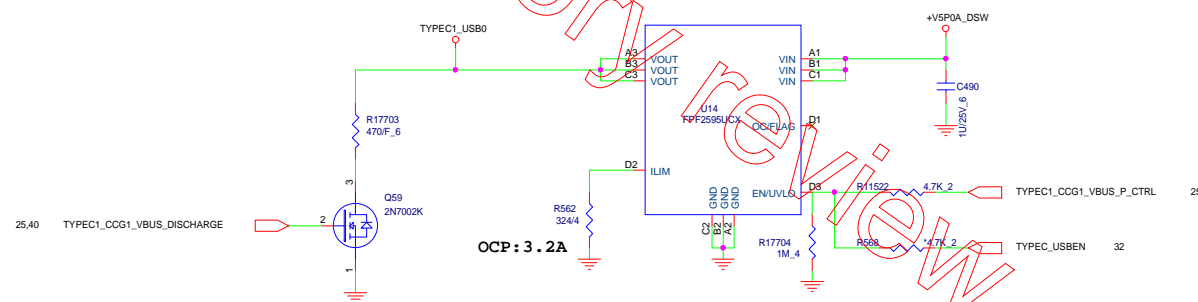
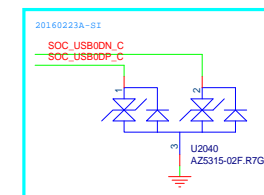
For LID power to Power/B



SPEAKER AUDIO AMPLIFIER





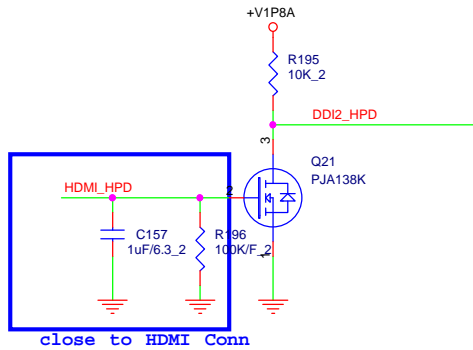


HDMI HOT PLUG

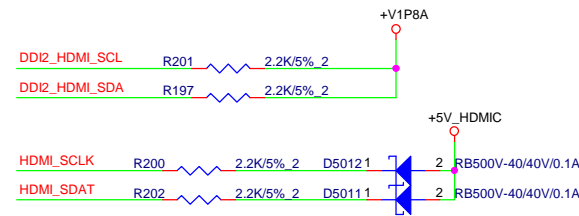
7	DDI2_TX2_N	DDI2_TX2_N	C315	0.1uF/10V 2C_TX0_HDMI-
7	DDI2_TX2_P	DDI2_TX2_P	C313	0.1uF/10V 2C_TX0_HDMI+
7	DDI2_TX1_N	DDI2_TX1_N	C312	0.1uF/10V 2C_TX1_HDMI-
7	DDI2_TX1_P	DDI2_TX1_P	C311	0.1uF/10V 2C_TX1_HDMI+
7	DDI2_TX0_N	DDI2_TX0_N	C310	0.1uF/10V 2C_TX2_HDMI-
7	DDI2_TX0_P	DDI2_TX0_P	C309	0.1uF/10V 2C_TX2_HDMI+
7	DDI2_TX3_N	DDI2_TX3_N	C317	0.1uF/10V 2C_TXC_HDMI-
7	DDI2_TX3_P	DDI2_TX3_P	C316	0.1uF/10V 2C_TXC_HDMI+

9	DDI2_HDMI_SCL	DDI2_HDMI_SCL	
9	DDI2_HDMI_SDA	DDI2_HDMI_SDA	
7	DDI2_HPD	DDI2_HPD	

HDMI HOT PLUG



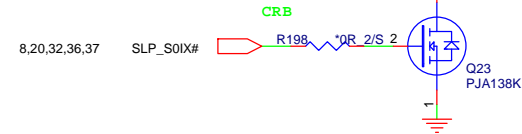
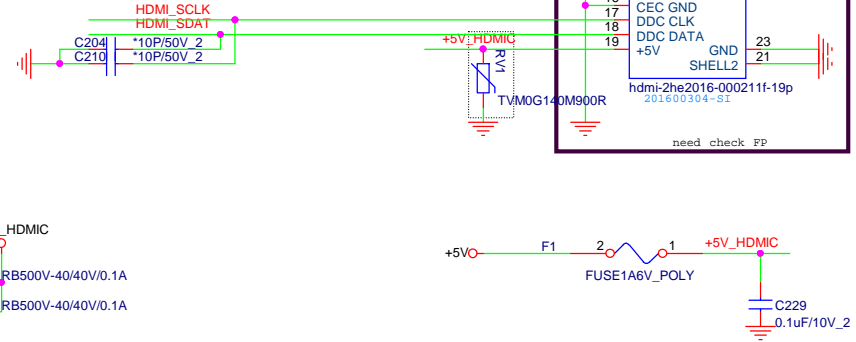
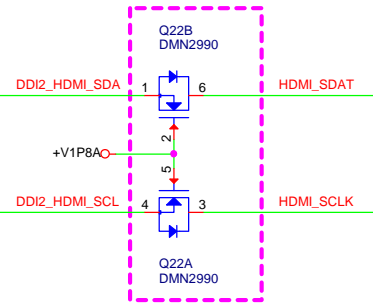
I2C Pull up




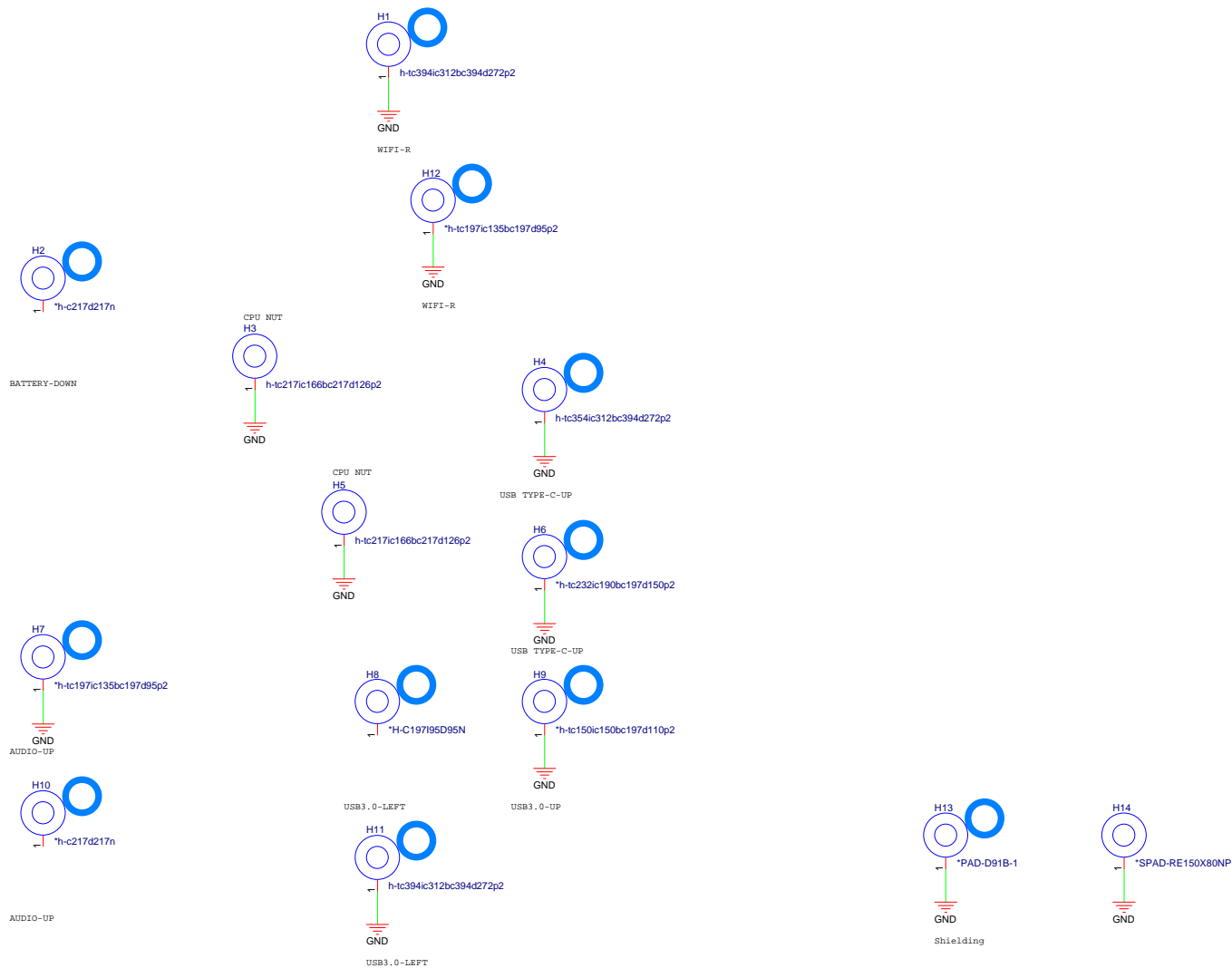
EMI Solut i on

C_TX2_HDMI+	R395	120/F 2	C_TX2_HDMI-
C_TX1_HDMI+	R400	120/F 2	C_TX1_HDMI-
C_TX0_HDMI+	R403	120/F 2	C_TX0_HDMI-
C_TXC_HDMI+	R409	120/F 2	C_TXC_HDMI-

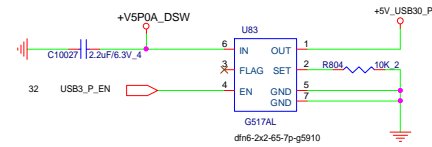
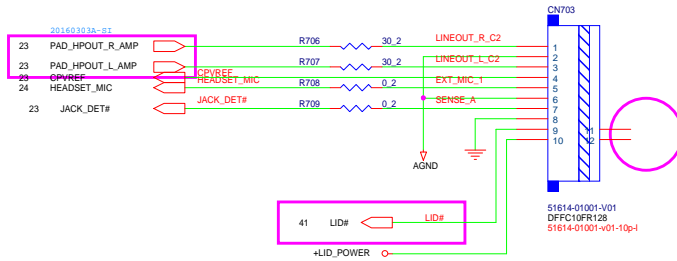
10/21 modify



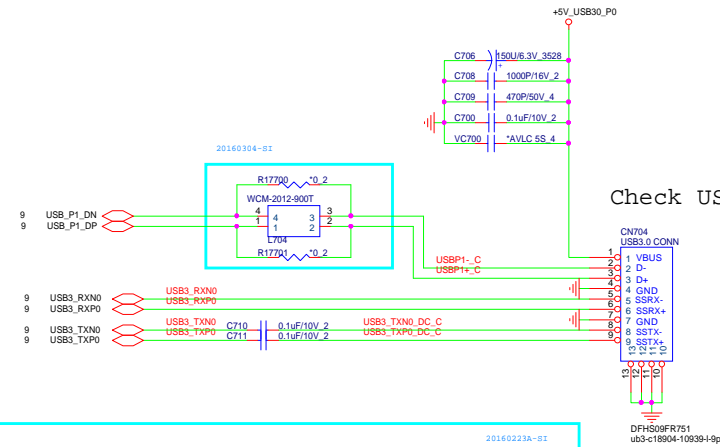
 Quanta Computer Inc. PROJECT : D91B		
Size B	Document Number Micro HDMI	Rev. 1A
Date: Wednesday, March 09, 2016	Sheet : 27 of 41	



Check Audio/B CN



Check USB3.0 Type-A CN



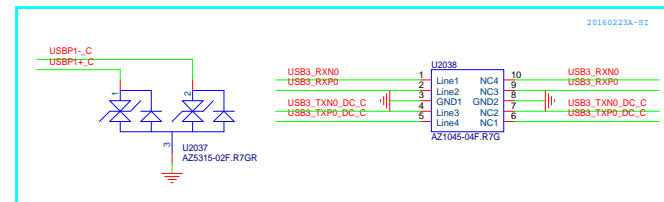
CHECK
DFHS09FR659
ub3-c190j8-90909-1-9p

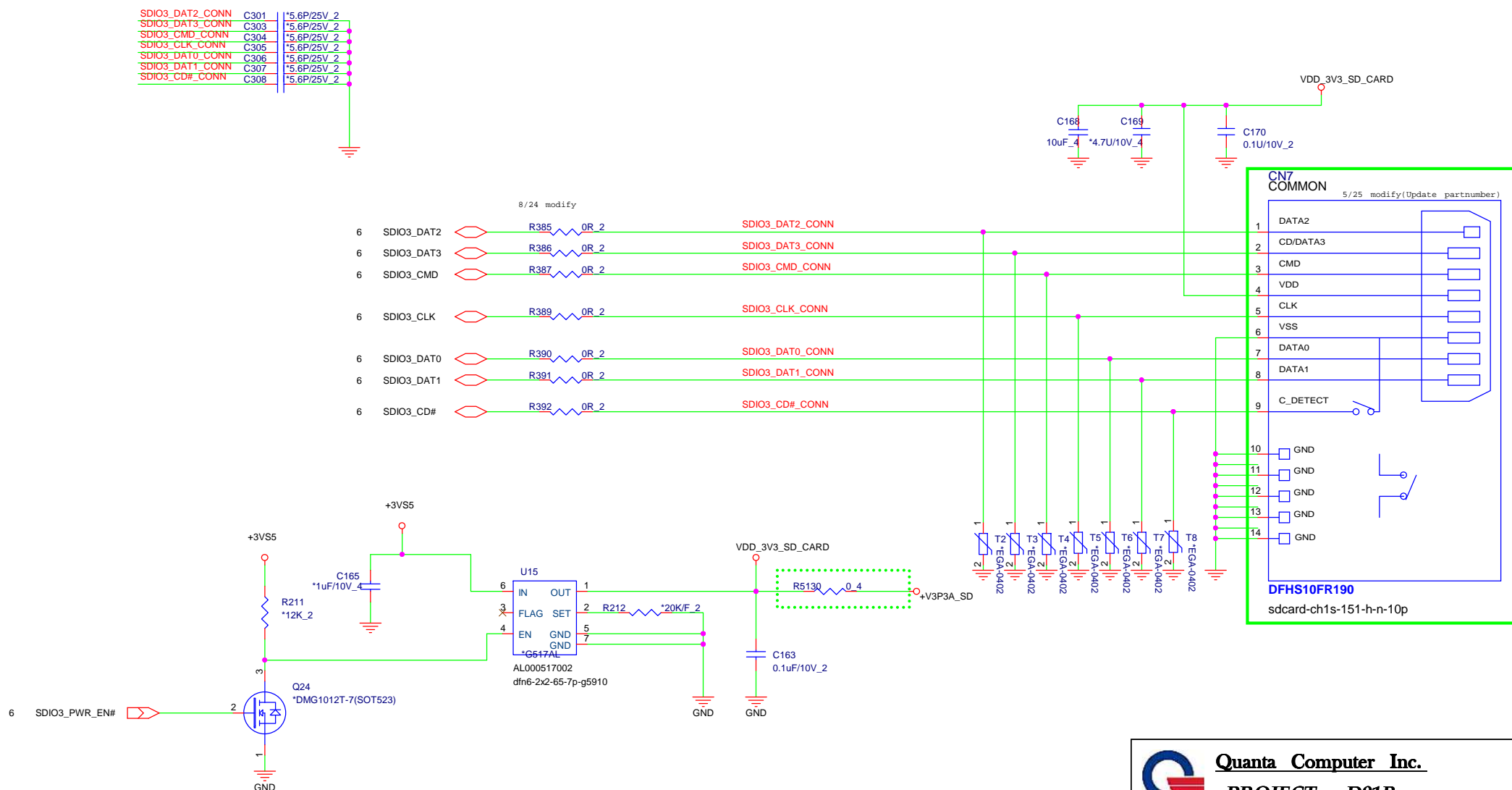
Table 4. C1 pin controls long/medium/short traces

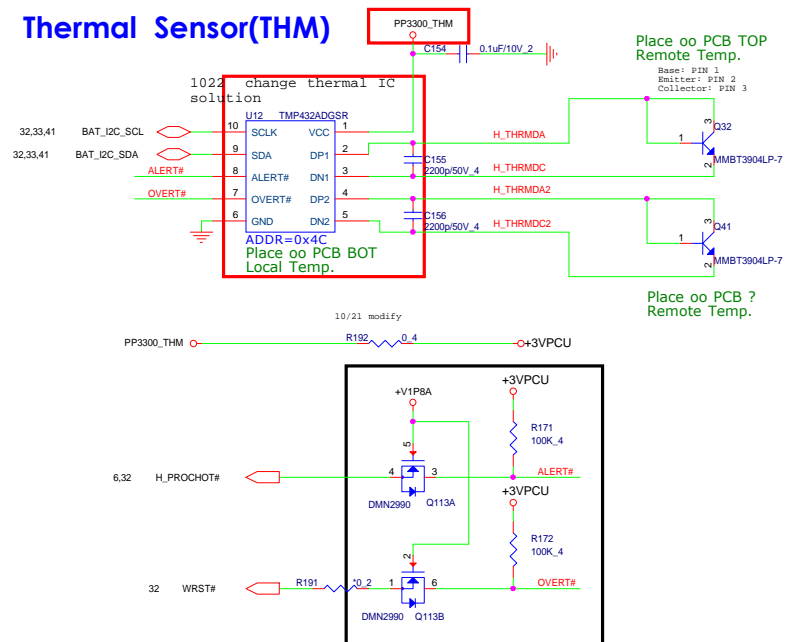
State	Channel type	Pin C1 state	Channel B	Channel A
			EQ ^[1]	DE ^[2] OS ^[3]
H	Long	H	9 dB	-5.3 dB 1.1 V
high-Z	Medium	high-Z	6 dB	-3.1 dB 1.0 V
L	Short	L	3 dB	0 dB 0.9 V

Table 5. C2 pin controls long/medium/short traces

State	Channel type	Pin C2 state	Channel A	Channel B
			EQ ^[1]	DE ^[2] OS ^[3]
H	Long	H	9 dB	-5.3 dB 1.1 V
high-Z	Medium	high-Z	6 dB	-3.1 dB 1.0 V
L	Short	L	3 dB	0 dB 0.9 V







Accelerometer +e-Compass+Gyro Sensor

Keepout area is around 10mm

```

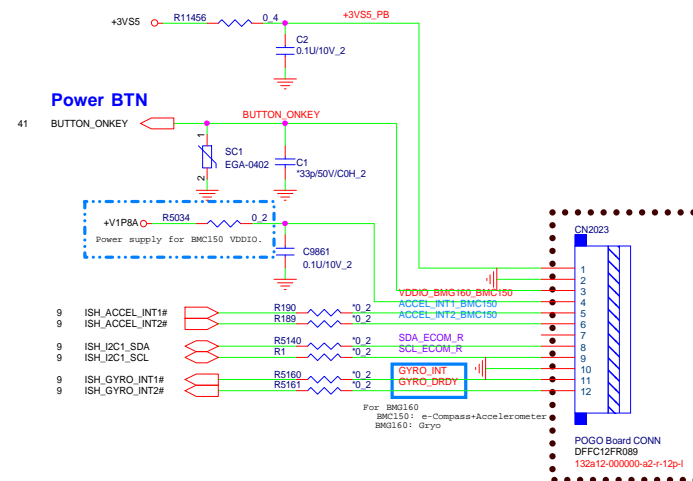
WR  Address : 0x3C
RD  Address : 0x3D

```

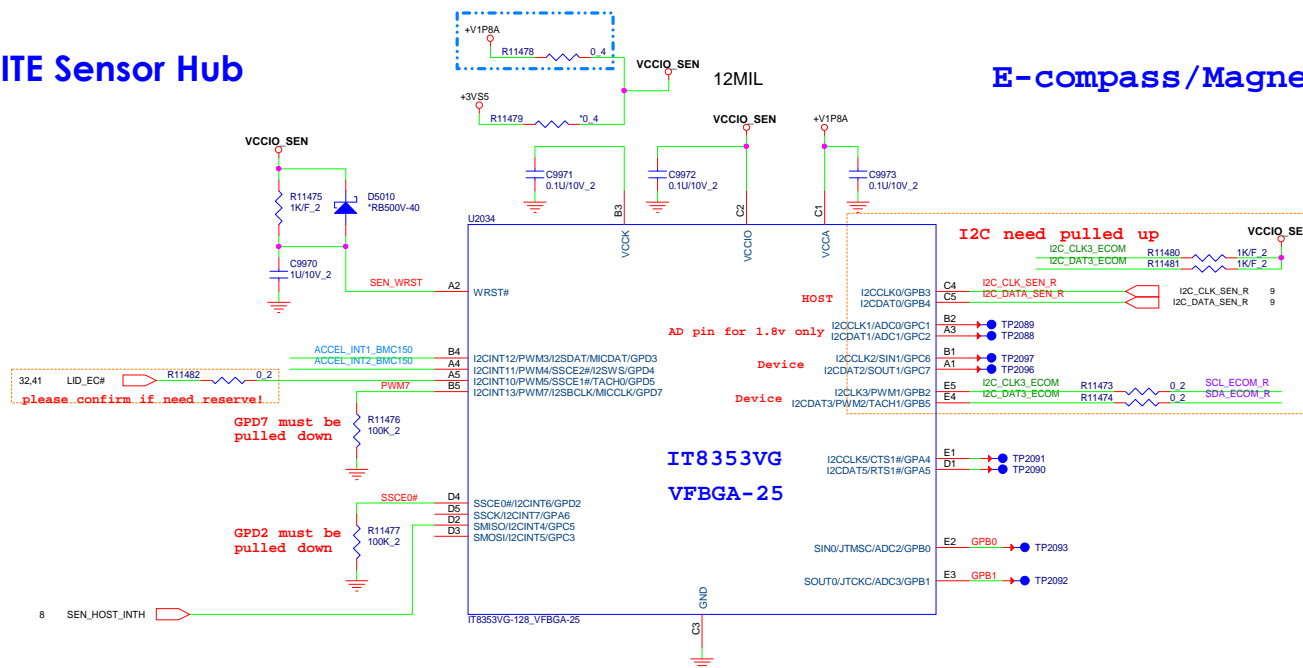
ALS

SDO_AG	AG Address
VDDIO	0x6B
GND	0x6A

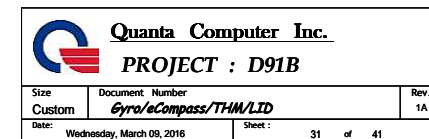
SDO_M	M Address
VDDIO	0x1E
GND	0x1C

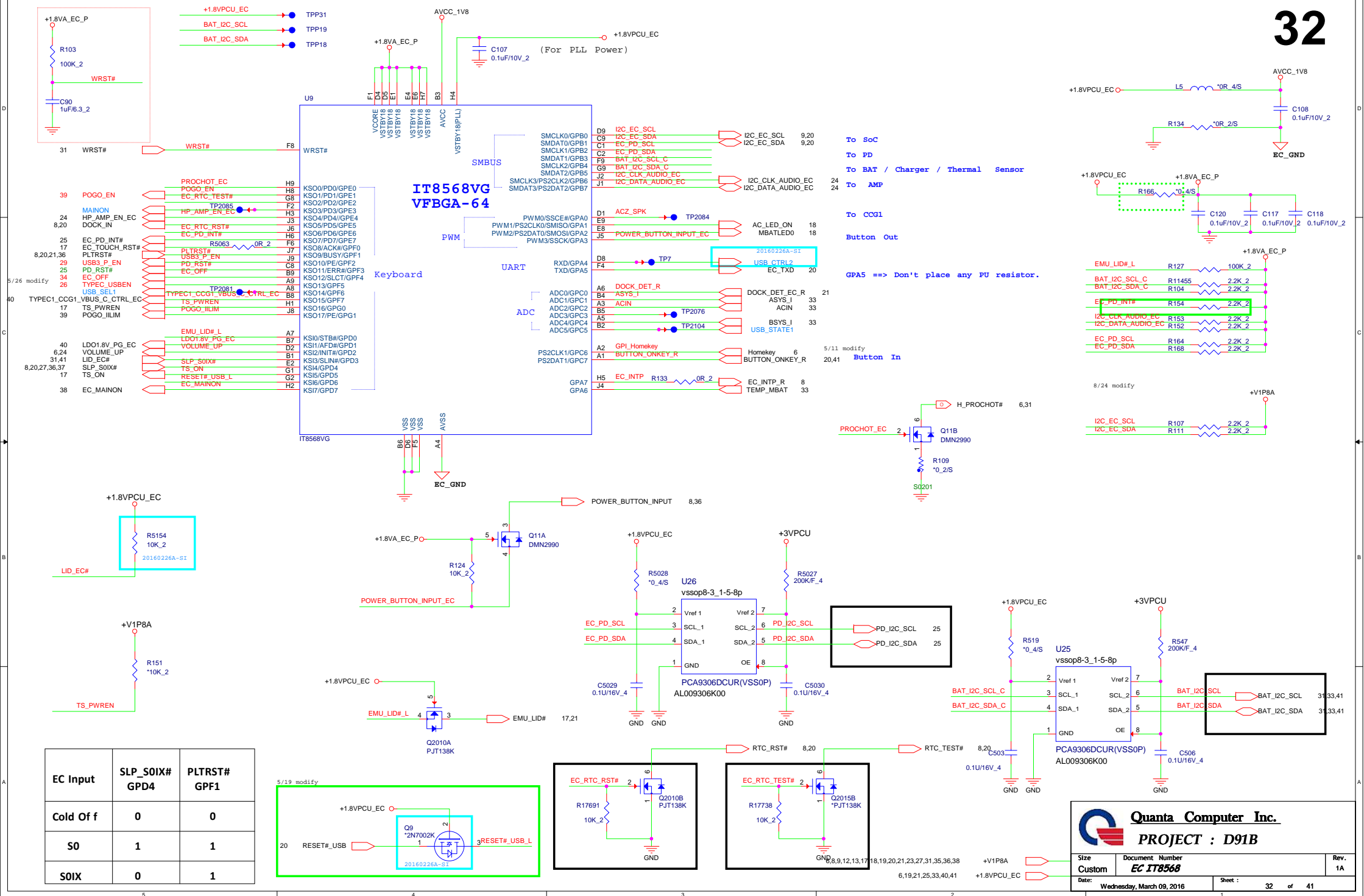


ITE Sensor Hub

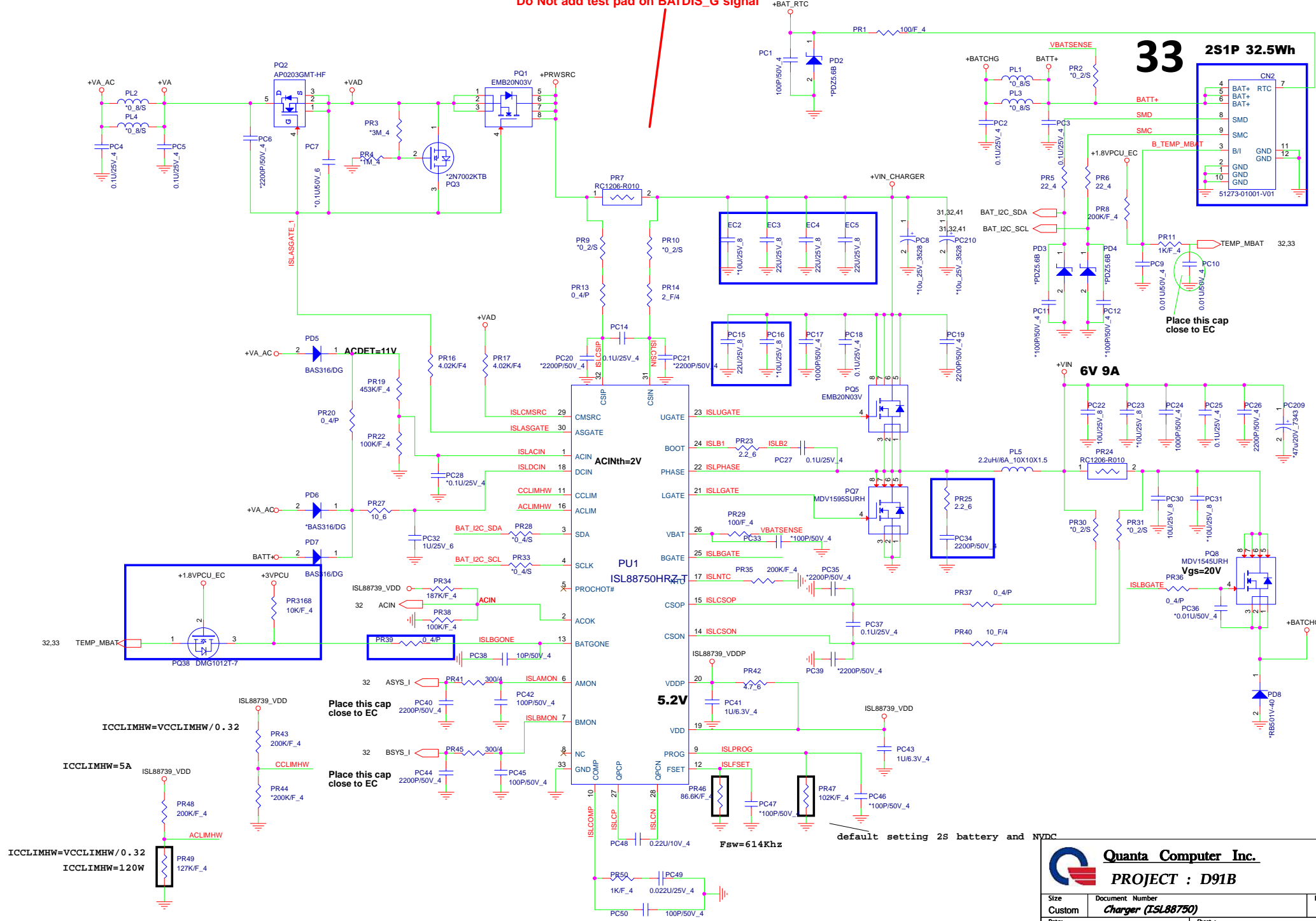


E-compass/Magnetometer/Accelerometer
(BMC150)





Do Not add test pad on BATDIS_G signal



33 2S1P 32.5Wh

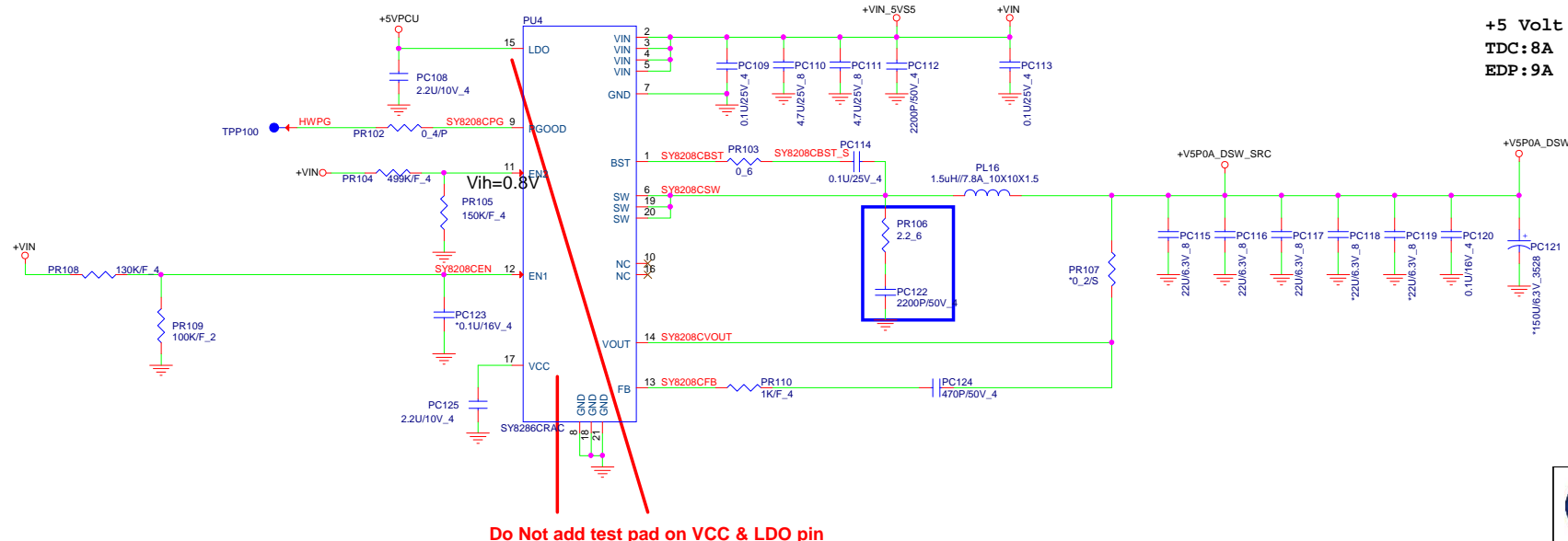
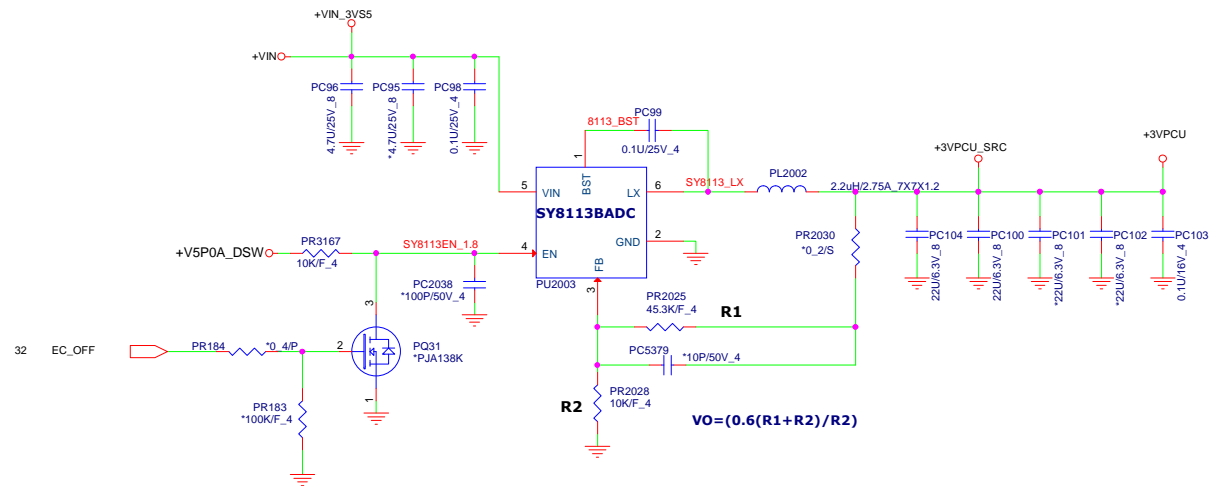
Pin 1: BAT+ RTC
Pin 2: BAT+
Pin 3: BAT+
Pin 4: SMD
Pin 5: SMD
Pin 6: SMD
Pin 7: B/I
Pin 8: GND
Pin 9: GND
Pin 10: GND
Pin 11: GND
Pin 12: GND

51273-01001-V01

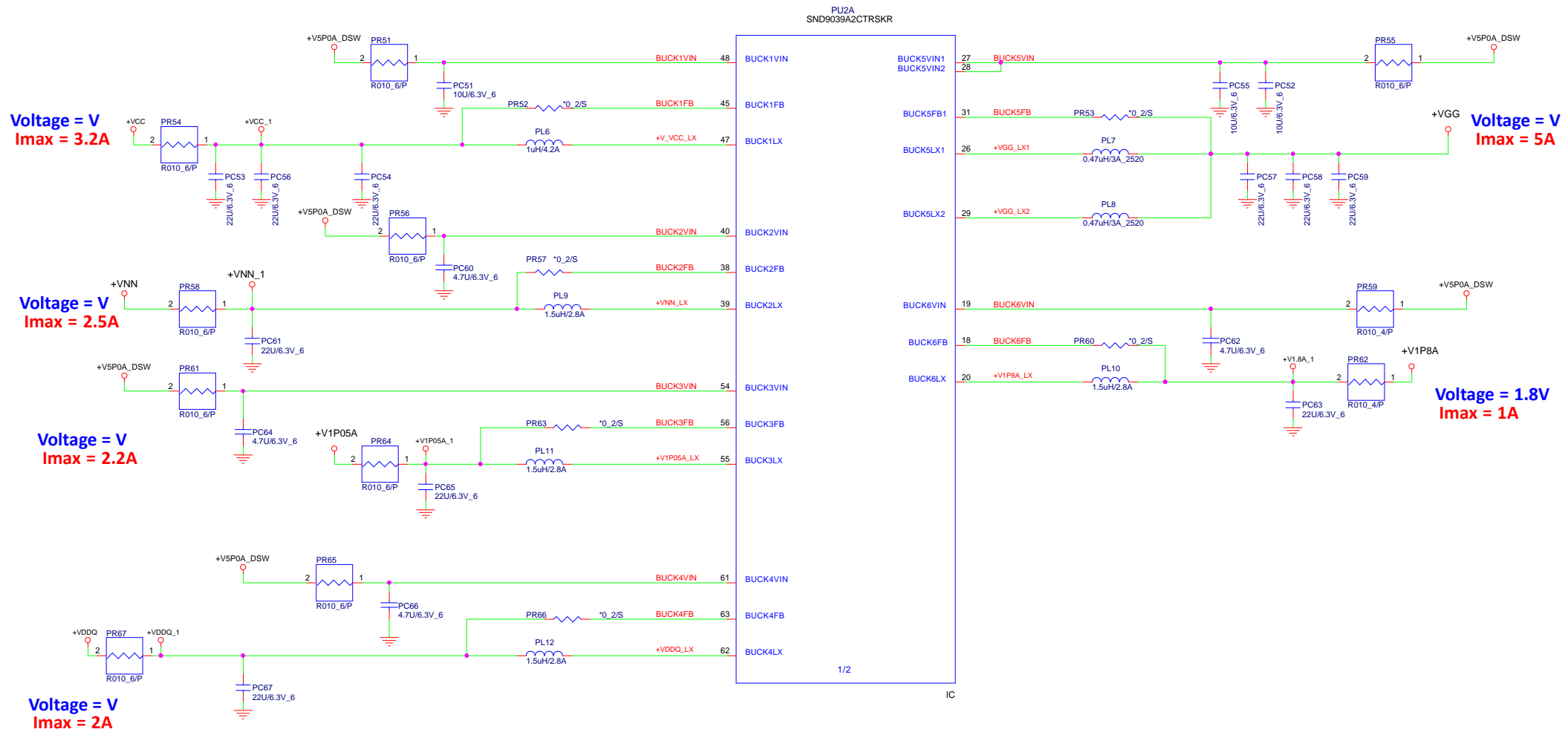
Place this cap close to EC

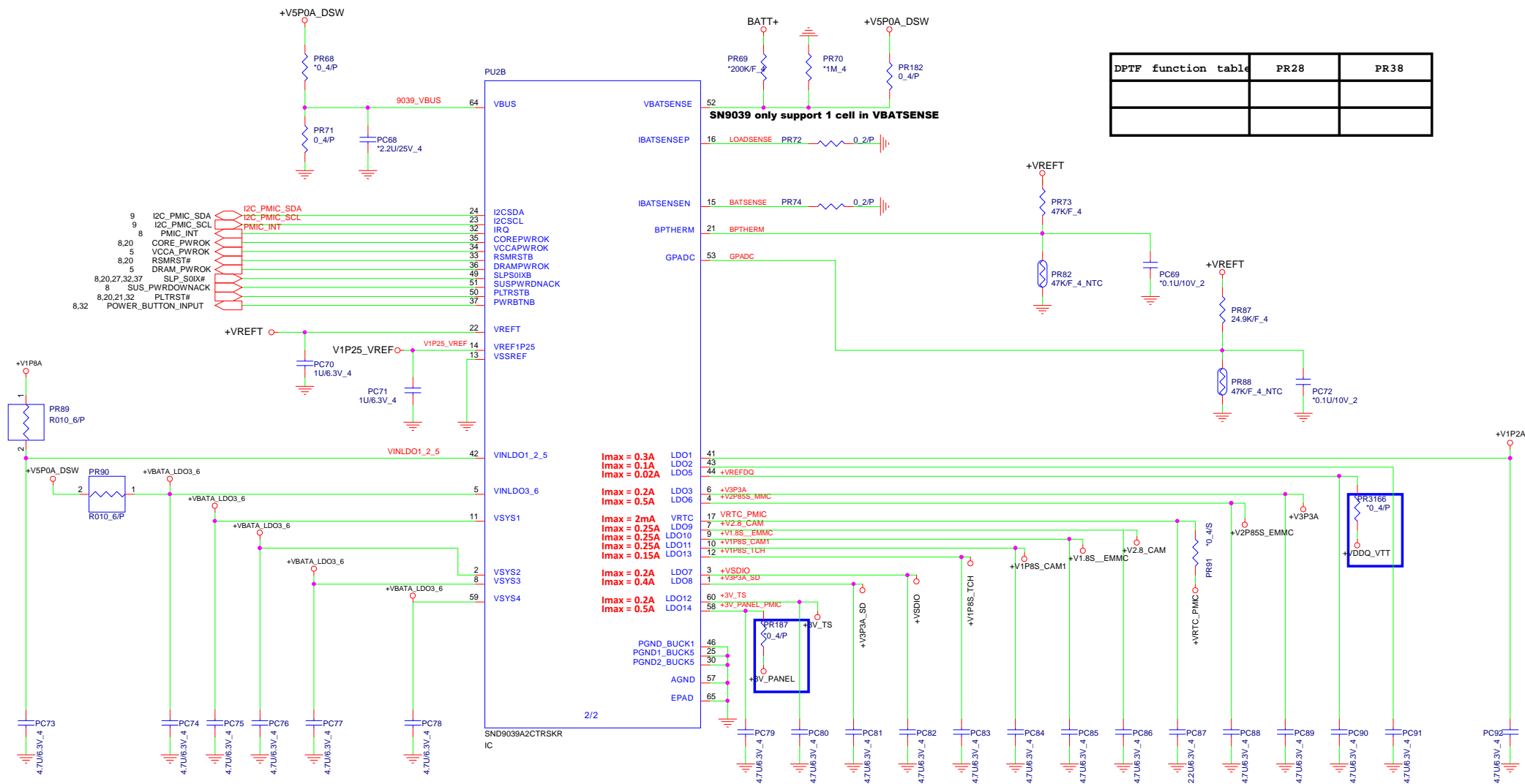
6V 9A

Pin 1: +VIN
Pin 2: +VAD
Pin 3: +VAD
Pin 4: +VAD
Pin 5: +VAD
Pin 6: +VAD
Pin 7: +VAD
Pin 8: +VAD
Pin 9: +VAD
Pin 10: +VAD
Pin 11: +VAD
Pin 12: +VAD
Pin 13: +VAD
Pin 14: +VAD
Pin 15: +VAD
Pin 16: +VAD
Pin 17: +VAD
Pin 18: +VAD
Pin 19: +VAD
Pin 20: +VAD
Pin 21: +VAD
Pin 22: +VAD
Pin 23: +VAD
Pin 24: +VAD
Pin 25: +VAD
Pin 26: +VAD
Pin 27: +VAD
Pin 28: +VAD
Pin 29: +VAD
Pin 30: +VAD
Pin 31: +VAD
Pin 32: +VAD
Pin 33: +VAD

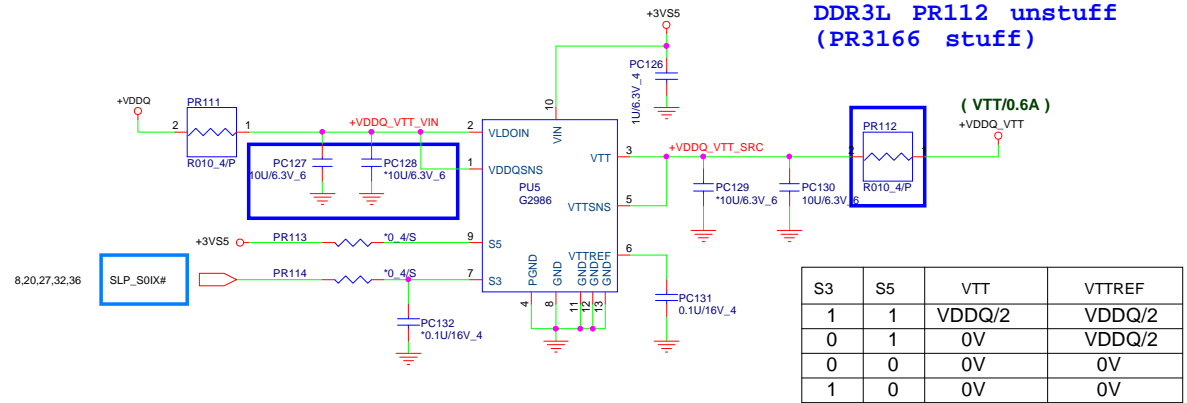


Cherry trail T3 :SND9039A2CTRSKR -> AL009039004
 Bay trail TCR : SND9039A2BT -> AL009039001

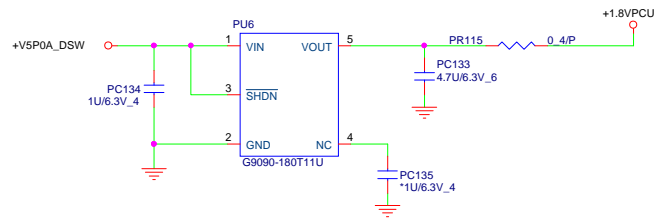


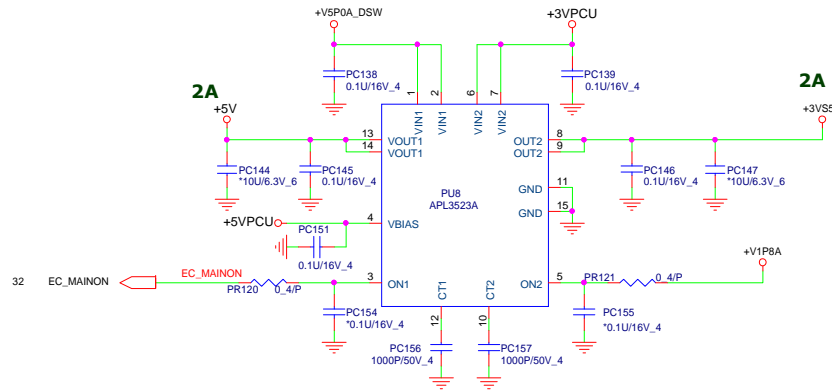


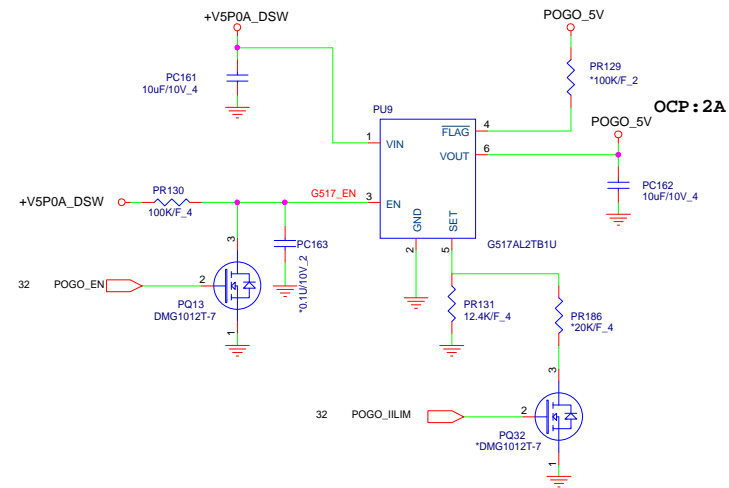
LPDDR3 PR112 stuff
(PR3166 unstuff)
DDR3L PR112 unstuff
(PR3166 stuff)



1.0mA







For TypeC PORT1

